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Synthesizing Fast Intrusion Prevention/Detection Systems from High-Level Specifications*

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Abstract

To build survivable information systems (i.e., systems that continue to provide their services in spite of coordinated attacks), it is necessary to detect and isolate intrusions before they impact system performance or functionality. Previous research in this area has focussed primarily on detecting intrusions after the fact, rather than preventing them in the first place. We have developed a new approach based on specifying intended program behaviors using patterns over sequences of system calls. The patterns can also capture conditions on the values of system-call arguments. At runtime, we intercept the system calls made by processes, compare them against specifications, and disallow (or otherwise modify) those calls that deviate from specifications. Since our approach is capable of modifying a system call before it is delivered to the operating system kernel, it is capable of reacting before any damage-causing system call is executed by a process under attack. We present our specification language and illustrate its use by developing a specification for the ftp server. Observe that in our approach, every system call is intercepted and subject to potentially expensive operations for matching against many patterns that specify normal/abnormal behavior. Thus, minimizing the overheads incurred for pattern-matching is critical for the viability of our approach. We solve this problem by developing a new, low-overhead algorithm for matching runtime behaviors against specifications. A salient feature of our algorithm is that its runtime is almost independent of the number of patterns. In most cases, it uses a constant amount of time per system call intercepted, and uses a constant amount of storage, both independent of either the size or number of patterns. These benefits make our algorithm useful for many other intrusion detection methods that employ pattern-matching. We describe our algorithm, and evaluate its performance through experiments.

1 Introduction

Our increasing reliance on networked information systems to support critical infrastructures (e.g., telecommunication, commerce and banking, power distribution, and transportation) has prompted interest in making the information systems survivable, so that they continue to perform their primary functions even in the face of coordinated attacks. In order to build survivable systems, it is necessary to detect and isolate attacks before they impact system performance or functionality. Thus a number of recent research efforts have focussed on the problem of intrusion prevention [GWTB96, SBS99, CPMWBBGWZ98, ML097, FBF99].

Many known approaches for intrusion prevention (including the one described in this paper) are based on the following observation about attacks: regardless of the nature of an attack, damage can ultimately be caused only via system calls made by processes running on the attacked host. It is hence possible to prevent damage due to attacks if we can monitor every system call made by every process, and prevent damage-causing calls from being executed. Actions to contain or respond to the attack could be launched at this point, e.g., terminating the process. Some of the central problems in the context of these approaches are:

- efficient interception of system calls
- pattern languages to characterize normal/abnormal system call sequences for any given program/process
- efficient matching of actual system calls made by a process against such patterns

Several techniques for system call interception have already been proposed in [GWTB96, ML097, *This research is supported in part by Defense Advanced Research Agency’s Information Technology Office (DARPAT-FO) under the Information System Survivability program, under contract number F30602-97-C-0244.
GPRAS, FBF99] This paper addresses the remaining two issues, focusing particularly on an expressive and easy-to-use specification language, and pattern-matching algorithms that are fast enough to be invoked on every system call.

1.1 Summary of Results

- In Section 2 we present a new and expressive language for capturing patterns of normal or abnormal behaviors of processes in terms of sequences of system calls and their arguments. As compared to the language described in [SCS98, SBS99], this paper focuses on a core language that we call regular expressions for events (REE). REEs extend regular expressions to model system calls that are characterized by a name as well as argument values. Response actions can be associated with patterns, and these will be launched automatically when our system observes a match for the pattern.

- In Section 3 we illustrate our language with several simple examples. We then detail the process of developing a complete specification for the ftpd server. This example, together with similar specifications for httpd and telnetd forms the basis of our experimental results in later sections.

- REEs have much higher expressive power than regular expressions. The presence of variables makes them comparable to attribute grammars in terms of expressive power. The examples in Section 3 illustrate that in practice, REE language provides the capabilities needed to describe program behavior as needed for intrusion detection or prevention.

- In Section 4 we present our runtime model for fast pattern-matching, based on extended finite-state machines (EFSA). Just as REE’s extend the power of regular expressions to permit variables, EFSA extend traditional finite-state automata to be able to assign or examine values of a finite set of variables. As in the case of regular expressions, every REE can be matched by a non-deterministic EFSA (NEFA) whose size is linear in the size of REE. But simulation of a NEFA at runtime can be very inefficient. We then examine the properties of deterministic EFSA (DEFA) which can be simulated efficiently. However, we show that the size of deterministic EFSA can be far too large (super-exponential in the size of NEFA) for the approach of using DEFA to be viable.

- Ideally, we would like an approach that achieves a balance between the space explosion implied by DEFA with the runtime inefficiency imposed by NEFA. We propose an algorithm to accomplish this in Section 5. Given an REE of size N, our algorithm produces an EFSA with a worst-case size that is $O(2^N)$. We show that this EFSA is deterministic for a subclass of REE’s called REE(0) patterns.

- We present an implementation of our techniques in Section 6 and report its performance. In practice, our algorithm uses much less than exponential amount of space. This means that we can perform intrusion prevention/detection in essentially constant time per system call for most patterns. Our current implementation introduces about 1 to 2% overhead due to the pattern-matching operations.

The applicability of the techniques developed in this paper extends well beyond our system. For instance, many intrusion detection techniques (e.g., [Kumar95, PK92, Ko96]) are formulated using signature patterns that characterize attacks. In contrast with our approach, the performance of matching algorithms developed in these approaches worsens linearly with the number of patterns. By using the techniques developed in this paper, the performance of these approaches can be improved significantly.

2 Approach Overview

We model security-related behaviors in terms of sequences of (security-related) events. In general, events may be internal to a single process, e.g., a function call made by the process; or they may be externally observable, e.g., a message delivered to a machine, or a system call made by a process. We are particularly interested in the system call events, since it is possible to enforce secure behaviors if we can interpret and modify system calls. Our specifications characterize normal and/or abnormal behavior of programs as patterns over system call sequences. These specifications are compiled into optimized programs for efficient detection of deviations from the specified behavior. When discrepancies are detected at runtime, defensive actions are initiated to contain or isolate the damage.

Our intrusion detection/prevention system consists of an offline and a runtime component. The offline system generates detection engines from specifications, while the runtime system provides the execu-
tion environment for these engines. The input to the offline component is a specification $S_P$ for each program $P$ to be defended\footnote{This specification may be developed based on known information about $P$, obtainable from such sources as manual pages, security advisories etc.}. The offline component translates $S_P$ into a C++ class definition $C_P$. This C++ class simulates an extended finite state machine for matching the patterns in $S_P$ and initiating appropriate responses to intrusion attempts. This code is compiled and then linked with a runtime infrastructure to produce a detection engine.

Figure 1 shows the runtime operation of our system. When $P$ executes as process $P_j$, it is monitored using the detection engine $M_j$, which incorporates an instance of $C_P$. System calls made by $P_j$ are intercepted by the system call interceptor just before, and just after the system call's kernel level functionality is executed, and the system call information is passed to $M_j$. If $M_j$ matches a pattern, it invokes the action associated in $S_P$ with that pattern. This action would typically utilize the support functions provided by the runtime system to modify the just-initiated system call execution so as to prevent it from causing damage.

The 	extit{fork} system call is given special treatment. When process $P_j$ executes a fork system call, it results in two processes $P_j$ and $P_j'$. At this point, the detection engine monitoring $P_j$ also clones itself, with one instance monitoring $P_j$ and another monitoring $P_j'$. If one of these processes, say $P_j$, executes another program using the 	extit{execute} system call, we may switch to monitoring the new program with respect to a new specification as described later.

Frequently, security specifications for a process are completely independent of the actions of other processes. However, some specifications concern interactions among multiple processes. To monitor the behavior of concurrent processes, we use a coordinating monitor that communicates with the monitors for different processes to identify deviant behaviors. We express specification of process interaction using a language construct called an atomic sequence, described later. The coordinating monitor is used to detect violations of atomic sequences. While we describe the atomic sequence construct and discuss a possible implementation to support it, we have not yet implemented it. As such, we do not provide experimental results regarding this construct.

2.1 Specification Language

We provide an overview of the principal components of our specification language here. The main components of a specification include variable declarations and rules. Variables declared in this manner are global, as opposed to local variables whose scope is limited to a single rule. Such global variables are referred to as state variables, while local variables are called temporary variables. State variables are restricted to be of primitive types, i.e., long, integer, boolean, char, and unsigned versions of these. A rule is of the form $pat \rightarrow actions$. Here, $pat$ denotes a pattern on sequences of system calls. When a process is monitored using this specification and the process makes a sequence of system calls that matches $pat$, the responsive steps contained in $actions$ is initiated.

2.1.1 Event Patterns

Event patterns are built from events using sequencing operators. Events are of the form
**EventName**(*Arg_1, ..., Arg_n*). For each system call, we identify two events: the entry event which corresponds to the invocation of the system call, and the exit event which corresponds to the return from the system call. The entry event uses the same name as that of the system call, while the exit event is obtained by appending _exit to the system call name. *Arg_1, ..., Arg_n* denote the system call arguments. An event history is a sequence of events.

We define a special event **begin** which precedes any system call made by any process, and the event pattern **any** that stands for any event.

Sequencing operators are similar to those used in regular expressions, but operate on events with arguments. We refer to our pattern language as regular expressions over events (REE) to indicate this relationship. **Elementary patterns** in our language are of the form \( e(x_1, ..., x_n) \mid \text{cond} \), where \( \text{cond} \) is a boolean-valued expression on the event arguments \( x_1, ..., x_n \), any temporary variables that may appear earlier in a pattern, and state variables. The condition component can make use of standard arithmetic, comparison and logical operations and several support functions. The support functions allowed in a pattern correspond to “read” operations that do not modify the state of the monitored process. An example of such a function is **realpath()** which translates a file name into a canonical form that does not contain “.”, “.”, or symbolic links.

The meaning of event patterns and the sequencing operators is best explained by the following definition of what it means for an event history to match a pattern:

- **event occurrence**: \( e(x_1, ..., x_n) \mid \text{cond} \) is satisfied by the event history \( e(v_1, ..., v_n) \) if \( \text{cond} \) evaluates to **true** when variables \( x_1, ..., x_n \) are replaced by the values \( v_1, ..., v_n \).
- **event nonoccurrence**: \( \neg e(x_1, ..., x_n) \mid \text{cond} \) is matched by \( H \) if it does not match \( e(x_1, ..., x_n) \mid \text{cond} \).
- **sequencing**: \( \text{pat}_1 ; \text{pat}_2 \) is satisfied by an event history \( H \) of the form \( H_1 \cdot H_2 \) provided \( H_1 \) satisfies \( \text{pat}_1 \) and \( H_2 \) satisfies \( \text{pat}_2 \).
- **alternation**: \( \text{pat}_1 \mid \text{pat}_2 \) is satisfied by an event history \( H \) if either \( \text{pat}_1 \) or \( \text{pat}_2 \) is satisfied by \( H \).
- **repetition**: \( \text{pat} \ast \) is satisfied by an event history \( H_1 \cdot H_2 \cdot ... \cdot H_n \) if \( H_i \) satisfies \( \text{pat} \), \( \forall 1 \leq i \leq n \).
- **realtime constraints**: \( \text{pat within} \ t \) is satisfied by an event history \( H_1 \) if \( H_1 \) satisfies \( \text{pat} \) and the time interval between the first and last events in \( H_1 \) is less than or equal to \( t \).

**atomicity:** **nonatomic** \( \text{d in pat} \) denotes that accesses to data \( d \) be atomic in \( \text{pat} \), i.e., without any intervening operations by other processes that could modify this data.

When a variable occurs multiple times within a pattern, an event history will satisfy the pattern only if the history instantiates all occurrences of the variable with the same value. For instance, the pattern \( e_1(x) ; e_2(x) \) will not be satisfied by the event history \( e_1(a) ; e_2(b) \), but will be satisfied by \( e_1(a) e_2(a) \).

We relax the definition of the sequencing operators to minimize the need to include both entry and exit events in a pattern. For instance, when two system call entry events occur in sequence, we implicitly insert an exit event in the middle, e.g., \( \text{open} \mid \text{C}_1 ; \text{close} \mid \text{C}_2 \) is treated as equivalent to \( \text{open} \mid \text{C}_1 ; \text{open_exit} ; \text{close} \mid \text{C}_2 \). If the first event is an entry event while the second is an exit event for a different system call, then an exit event for the first call and the entry event for the second call are implicitly added, e.g., \( \text{open} \mid \text{C}_1 ; \text{close_exit} \mid \text{C}_2 \) is treated as equivalent to \( \text{open} \mid \text{C}_1 ; \text{open_exit} ; \text{close} ; \text{close_exit} \mid \text{C}_2 \).

The value of a temporary variable should be defined before its first use via a **binding** that takes the form \( \text{tvar} = \text{expr} \). Note that binding differs from assignment in that there can be at most one binding to any temporary variable, as subsequent conditions of the form \( \text{tvar} = \text{expr} \) are treated as comparisons.

### 2.1.2 Response Actions

The response action associated with a rule \( p \rightarrow a \) is launched if a **suffix of the event history matches** \( p \). The action component consists of a sequence of statements, each of which can either be an assignment to a state variable or invocation of a support function provided by the runtime system.\(^2\)

A typical response is to prevent a system call from executing and/or return a fake return value. This is accomplished using a support function **fail**, which takes an argument that corresponds to the value to which the variable **errno** should be set to. Sometimes, it is necessary (or just convenient) to switch to a new specification using the support function **switch(f)**, where \( f \) is the name of the new specification.

\(^2\)Knowledge about these support functions are not integrated into the specification language, but are declared in header files that can be included in the specification. Due to space constraints, we do not treat these declarations in detail.
If multiple patterns match at the same time, all of the associated actions are launched. This leads to a problem when some of these actions conflict with each other. It is possible to address such interactions by (a) defining a notion of conflict among operations contained in the reaction components of rules, whether they be assignments to variables or invocation of support functions provided by the run-time system, and (b) by stipulating that there must not exist two patterns with conflicting operations such that for some sequence of system calls, they can match at the same point. Potential conflicts can be identified by the automaton construction algorithms developed in this paper — if there is any state in the automaton that corresponds to a final state for two such patterns, then there is a potential conflict. However, we have not implemented this approach yet, instead relying on the specification writer to deal with such conflicts.

3 Example Specifications

We begin this section with a few simple examples and then proceed to give a complete specification for the FTP daemon.

3.1 Simple Examples

To restrict a program from making a set of system calls, we can create a simple pattern that matches any of these disallowed system calls and then invoke an action that causes these calls to fail. For instance, we may wish to prevent a server program such as fingerd from executing any program, modify file permissions, create files or directories or initiate network connections. We use the shorthand notation of omitting some of the trailing (and sometimes, all of the) variables of a system call when we are not interested in their values.

\[
\text{execve}[@\text{connect}|\text{chmod}|\text{chown}]\\n\text{creat}|\text{truncate}|\text{sendto}|\text{mkdir}\\n\to \text{fail}(\text{EINVAL})
\]

We may also wish to restrict the files accessed for reading or writing. For a program such as fingerd, we may use the following rule to prevent the program from writing any file, and reading any files other than those mentioned in admFiles defined below.

\[
\text{adminFiles} = \{
\text{"/etc/utmp"},
\text{"/etc/passwd"}, \text{datadir/*}
\}
\text{open}(f, \text{mode})|[(\text{realpath}(f) \notin \text{adminFiles})]
\]

\[
\to (\text{mode} \neq 0)\text{RONLY}
\]

\[
\to \text{fail}(\text{EPERM})
\]

To illustrate the use of sequencing operators, consider the following pattern that asserts that a program never opens and closes a file without reading or writing into it. Before defining the pattern, we define abstract events that denote the occurrence of one of many events. Occurrence of an abstract event in a pattern is replaced by its definition, after substitution of parameter names, and renaming of variables that occur only on the right-hand side of the abstract event definition so that the names are unique.

\[
\text{openExit} (fd) :=
\text{openExit}(f, fl, m, fd) | \text{creatExit}(f, m, fd)
\]

\[
\text{rwOp} (fd) := \text{read}(fd) | \text{readDir}(fd) | \text{write}(fd)
\]

\[
\text{openExit}(fd); (\text{rwOp}(fd)) * ; \text{close}(fd) \to \cdots
\]

Although regular expressions are not expressive enough to capture balanced parenthesis, the presence of variables in REE enables us capture the close system call matching an open. This issue of expressive power is discussed again later.

The example below illustrates the use of atomic sequence patterns. A popular attack uses race conditions in setuid programs as follows. Since the setuid process runs with effective user root, any open operation by the process will always succeed. If the process is running on behalf of a user, and wishes to open a file with the permissions of this user (i.e., with privileges corresponding to the real userid), it may do so by first using the access system call which determines if the real user has access to a file, and if so, it goes ahead and opens the file. The attacker exploits the time window between the access and open system calls as follows. The attacker uses a symbolic link as the name of the file in question, and changes the target of the link between the access and open system calls. To prevent this attack, we ensure that the object referred by the access and open system calls is accessed atomically:

\[
\text{nonatomic}(f, \text{target}) \in
\text{access}(f); \text{open}(f); *; \text{open}(f)
\]

\[
\to \text{fail}(\text{EACCESS})
\]

3.2 Case Study: Specification for ftpd

Our starting point in developing a specification of ftpd is the documentation provided in its manual pages. Specifically, we identified the following properties for wu-ftp by examining its manual page and based on our knowledge of UNIX. These properties are captured in our specification language in Fig-
Figure 2: A specification for ftppd.

ure 2. Although it is possible to turn the English descriptions directly into specifications in our language, it is usually necessary to cross-check (or “de-
bug”) the specifications by monitoring ftppd under typical conditions. We have therefore used a hy-
brid approach, where we first manually inspected system call traces produced by ftppd and used it to
further narrow down the actions/behaviors that the ftppd server may exhibit. In most cases, we have not
attempted a sophisticated response, instead opting for a simple action such as terminating the process
using a support function named term().

• ftppd attempts to authenticate a client host be-
fore proceeding to user authentication phase.
Precisely identifying the sequence of system calls
that correspond to client authentication is hard,
as it involves a large number of steps that may
vary from installation to installation. As such,
we treat getpeername as a marker that indicates
host authentication related processing. Similarly, we treat opening of /etc/passwd as a marker for user authentication related processing. Rule 13 captures this English description by stipulating that an open of the password file should never happen before invocation of getpwnam.

- users need to be first logged in before most files can be accessed. Rule 14 uses setreuid as an indicator for completion of user login process.
- after user authentication is completed, ftpd sets the userid to that of the user that just logged in. We remember this userid for later use (rule 11).
- prior to user authentication, only files beginning with names identified in the set ftpadmFilePrefixes can be accessed (rule 15).
- certain system calls are never used before user login, and certain others are never used after login process (rules 16, 17). Let ftpInitBadCall denote a pattern that matches system calls not used prior to user login. Similarly, let ftpAccessBadCall match system calls that are not used after login\(^3\).
- for anonymous login, the userid FTPUSERID is set; moreover, the chroot system call is used to restrict access only to the subtree of the filesystem rooted at ”ftp” (rule 18).
- ftpd resets its effective userid to root in order to bind certain sockets to ports numbered below 1024. The userid is reverted back to that of the logged in user immediately afterwards (rule 20).
- to eliminate possible security loopholes, ftpd must execute a setuid system call to change its real, effective and saved userid permanently to that of the logged in user before executing any other program; otherwise, the executed process may be able to revert its effective userid back to that of superuser (rule 19). In addition, we make sure that any file that is opened with superuser privilege is closed before exec (rule 21).
- finally, we model certain site-specific policies that override any access policies configured into ftpd. These policies are captured by rules 22 through 25.

3.3 Discussion

We make the following observations about the specification for ftpd.

- In order to reduce clutter, we have deliberately abbreviated some of the lists (e.g., ftpInvalidUsers), while leaving out definitions of some abstract events (e.g., wrOpen) in the specification for ftpd.
- Typical specifications need not be as comprehensive as for ftpd – we have made it comprehensive in order to better illustrate what sorts of properties can be captured in our language.
- The specification was developed using the principle of least privilege, without really paying attention to known vulnerabilities. Nevertheless, it does address most known ftp vulnerabilities (many of which have since been fixed) such as FTP bounce (rule 25), race conditions in signal handling (rules 20, 19) and site-exec (rule 24) [CERT].
- Availability of fast matching algorithms described in the subsequent sections enables us to focus on developing these rules independent of each other, as opposed to worrying about how they may be modified to enable more optimal checking, e.g., rules 11, 15, 16, and 23 have prefixes in their pattern component that are similar, and we can in fact combine them into a single rule. But this will lead to a specification that is much less clear, so we avoid this. Since the matching algorithms give us the benefit of such combination for free, there is no cost to pay for the clarity of specifications.
- We can develop a more concise language that gives us the ability to specify dependencies among events \(e_1\) and \(e_2\) more directly, rather than using a pattern such as begin(\(\langle e_1 \rangle \); e\(\_2\)). However, the focus here is on a core language that has the necessary expressive power.
- The examples illustrate that the expressive power of REE is far beyond that of regular expressions. For instance, rules 20 and 21 capture associations among events that are beyond what can be captured even by context-free grammars. (The associations are similar to checking that a variable is defined before use, and can be captured by attribute grammars.)

4 Runtime Model

Our runtime model for the detection engine is based on extended finite-state automata (EFSA). EFSA are simply standard finite state automaton that are augmented with the ability to store values in a fixed number of state variables. Every transition in the
EFSA is associated with an event, an enabling condition involving the event arguments and state variables, and a set of assignments to state variables. The final states of the EFSA may be annotated with actions, which, in our system, will correspond to the response actions given in our specifications. For a transition to be taken, the associated event must occur and the enabling condition must hold. When the transition is taken, the assignments associated with the transition are performed.

An EFSA is normally nondeterministic. The notion of acceptance by a nondeterministic EFSA (abbreviated as NEFA) is similar to that of an NFA: a NEFA accepts an event history \(e_1e_2\cdots e_n\) if there is a sequence of states \(s_0, s_1, \ldots, s_n\) such that \(s_0\) is the start state, \(s_n\) is a final state, and \(\forall 1 \leq i \leq n, \text{there exists a transition from } s_{i-1} \text{ to } s_i \text{ that can be taken on } e_i\). A deterministic EFSA (DEFA for short) is an EFSA in which at most one of the transitions is enabled in any state of the EFSA. A NEFA for the pattern \(a(x); b*; b(x)\) is shown in Figure 3. The equivalent DEFA is also shown in the same figure.

We summarize some of the key properties of REE and EFSA, stated without proof. Let the REE be of size \(N\), and use \(k\) variables \(X_1, \ldots, X_k\) where \(X_i\) can assume \(n_i\) distinct values, for \(1 \leq i \leq k\).

- there exists a NEFA of size \(N\) corresponding to the REE and it uses state variables \(X_1, \ldots, X_k\)
- this NEFA can be simulated with at most \(O(N \times n_1 \times n_2 \cdots \times n_k)\) cost per event at runtime
- every NEFA can be transformed into an equivalent DEFA with the same set of state variables
- in the worst case, the smallest DEFA for the above REE will have \(2^{N \times n_1 \times n_2 \cdots \times n_k}\) states

The first and third properties are among those that carry over from regular expressions to REE. The second property shows that the overhead for simulating NEFA at runtime can be significantly higher than that for simulating NFA, the latter being just \(O(N)\). In particular, given an RE of size \(n\) and a string of size \(m\), we can determine whether \(m\) matches \(n\) in \(O(mn)\) time. We simply construct the NFA for RE. This step takes \(O(n)\) time and constructs an NFA with \(O(n)\) states. While simulating the NFA, note that the NFA may potentially be in any of the \(n\) states, so we need to compute transitions from each of the \(n\) states on a given input symbol. Thus the processing of \(m\) input symbols will take \(O(mn)\) time. In contrast:

**Lemma 1** The problem of matching REE is NP-complete.

The proof follows readily from a similar result for matching regular expressions with back references (also known as rebr's) [Aho00].

We note that this result does not conflict with the fact that a DEFA for an RE can determine whether an input event history of length \(l\) matches the REE in \(O(l)\) time. However, unless \(P=NP\), one would have to spend an exponential time in the worst case to construct the DEFA. This is consistent with our earlier result regarding the size of DEFA.

Another way to look at the problem of NEFA simulation is that whereas an NFA's state was determined just by its control state, a NEFA's state is determined by its control state plus the values of its state variables. Thus, the total number of states of a NEFA is \(N = n \times m_1 \times \cdots \times m_k\) if the NEFA has \(n\) control states and \(k\) state variables, with the \(i\)th variable capable of taking up to \(m_i\) values. Consequently, the overhead for simulating each step of the NEFA may take \(O(n \times m_1 \times \cdots \times m_k)\) time. Note that if \(k, m_1, \ldots, m_k\) are small, the overhead for simulation can become small.

Below we present an algorithm for using NEFA for matching event histories at runtime.

The algorithm uses a hash table to store all of the possible states in which the REE could be in. On receipt of an event, we compute the new states of the NEFA by making a transition from each of the current states. A state of the NEFA is represented by a tuple \((c, v_1, \ldots, v_n)\) where \(c\) denotes the control state of the NEFA and \(v_1, \ldots, v_n\) denote the values of the state variables. The algorithm uses a function \(\epsilon\) to compute the set of states reachable from a state \(S\) while only following \(\epsilon\)-transitions.

We remark that our NEFA model has some similarities with the colored Petri net model used in [Kumar95]. In particular, the notion of a token
procedure NEFAtrans(HashTable curStates, Event e)
    foreach state \( S \equiv (c, v_1, \ldots, v_n) \in \text{curStates} \) do
        delete \( S \) from \( \text{curStates} \)
    endforeach
    transition \( T \) from \( c \) to a state \( c' \) enabled on \( e \) with value \( v_i \) for state variable \( x_i \) do
        let \( v'_1, \ldots, v'_n \) be the new values of state variables after making the assignments in \( T \)
    endforeach
    \( S' \in \epsilon(s'([c', v'_1, \ldots, v'_n])) \) that is not already in \( \text{curStates} \) do
        add \( S' \) to \( \text{curStates} \)
    end
end

procedure NEFAsim(EventHistory \( H \))
    let \( S' \equiv (c', v'_1, \ldots, v'_n) \) denote the initial state
    let \( H \) be of the form \( e_1, e_2, \ldots, e_m \)
    initialize a HashTable \( \text{curStates} \) to contain \( S' \)
    for \( 1 \leq j \leq m \)
        \( \text{NEFAtrans(curStates, } e_j) \)
    end
end

in their Petrinet corresponds to our notion of a NEFA state. Their semantics of matching differs from ours in some ways, and as a result, their algorithm for simulating a nondeterministic Petrinet results in cloning of tokens at each step of the simulation algorithm. This can result in unbounded increase in the number of tokens, whereas the number of distinct states of the NEFA is bounded as discussed above.

Another problem is that the size explosion due to NEFA to DEFA conversion can be significantly worse than NFA to DFA conversion, the latter explosion being limited to \( O(2^N) \) in the worst case, and much smaller in practice. In fact, the explosion for NEFA to DEFA construction is unacceptably large, making such conversion impractical. We tackle this problem in the next section.

**Theorem 2** The worst-case lower bound on the size of \( \text{DEFA} \) for an REE with \( k \) variables \( X_1, \ldots, X_k \) such that \( X_i \) can assume \( n_i \) distinct values (for \( 1 \leq i \leq k \)) is \( 2^n_1 \cdots n_k \).

The proof is by identifying a class of REE that brings out the worst-case behavior. One such class is given by \( (a(b))s; b(a(X_1); a(X_2); \ldots; a(X_k); b; \)
\( (a(b))s; a(X_1); a(X_2); \ldots; a(X_k) \)

**Proof sketch:** We construct REE with \( k \) variables for which we show that the size of the smallest DEFA is \( 2^n_1 \cdots n_k \). The REE we construct will be \( R \equiv (a(b))s; b(a(X_1); a(X_2); \ldots; a(X_k)) \).

Let \( V \) denote the set of all strings \( a(v_1)a(v_2) \cdots a(v_k) \) where \( v_i \) is any one of the values that can be taken by \( X_i \). Note that \( |V| = n_1 \cdots n_k \). Let \( V \) denote any subset of \( V \). Let \( s_V \) denote a string that contains \( \text{leb} \) as a substring for every \( v \in V \) as a substring, but not for \( v \in V - V \). Note that on inspecting \( s_V \), any DEFA for \( R \) must reach a state \( S_V \) such that there is a path from \( S_V \) to a final state on inspecting any string in \( V \), but no such path exists for any \( v \in V - V \). For this reason, the states \( S_V \) is not equivalent to \( S_{V'} \) whenever \( V \neq V' \). Since the number of distinct subsets of \( V \) is \( 2^n_1 \cdots n_k \), the DEFA must have at least that many distinct states. Note that since a DEFA's state is characterized by a control state plus the values of variables \( X_1, \ldots, X_k \), the same control state may correspond to \( n_1 \times n_2 \times \cdots \times n_k \) states, corresponding to the different possible values of these variables. Thus, the number of control states in the DEFA must be at least \( 2^n_1 \cdots n_k \).

5 Translation of REE to NEFA

We propose an algorithm that achieves a balance between the space explosion implied by DEFA and the runtime inefficiency imposed by NEFA in this section. The key point is that the explosion due to state variables \( 2^n \) is unacceptable, whereas the explosion due to the size of REE \( (2^N) \) does not usually pose a problem in practice. Our algorithm is thus geared towards avoiding size explosion due to state variables. This is achieved by permitting nondeterminism in the automata on a subset of transitions where state variables are being assigned new values. This approach results in an EFSA that is deterministic for a subclass of REEs called REE(0) patterns. For non-REE(0) patterns, the EFSA produced may not be deterministic, but our experimental results show that the performance of the algorithm is very good even for such patterns. Regardless of the nature of the patterns, the EFSA size is \( O(2^N) \). We present an overview of our algorithm here, leaving out the details due to space constraints. (See [SU90] for a more complete description.)

5.1 Translation Algorithm

Our algorithm has an initial preprocessing phase that includes the following steps:
• strip the leading \textit{begin} from patterns if a pattern does not start with a \textit{begin}, prefix \textit{any*}, so that the augmented pattern will match an event history iff the original pattern matched a suffix of the history.
• introduce an end-marker \# to every pattern, and then combine them into one pattern using $\parallel$.
• express realtime or atomicity using $*, ;, \parallel$ and constraints on argument values
• rename event arguments so that \textit{ith} argument to any event is named $\&i$. For arguments that need to be remembered for later, we assign their values to state variables using notation $/t_j = \&i$. A pattern that does not use a state variable after this step is known as an REE(0) pattern.
• number the positions in the REE from left to right, indicating the positions as superscripts.

In case atomicity or realtime constraints are used there are additional preprocessing steps as described below.

**Realtime Constraints** For realtime constraints, we may view that all events are time-stamped, with the time provided as another event argument. We can then transform a pattern such as $e_1(x_1,\ldots,x_n);e_2(y_1,\ldots,y_m)\textbf{within }t$ into $e_1(x_1,\ldots,x_n,t_1);e_2(y_1,\ldots,y_m,t_2)|(t_2 - t_1 \leq t)$.

**Atomicity Constraints** Here we make use of the coordinating monitor to detect violations. The overall approach is as follows. As soon as the first event in an atomic sequence \textbf{nonatomic }d in pat is observed by a monitor $M_d$, it sends a message to the coordinating monitor indicating the beginning of an atomic sequence and the data item(s) to be accessed atomically during this sequence. The coordinating monitor broadcasts its interest on operations that modify these data items to all other monitors. If a monitor $M_d$ detects that a modification operation is already underway, or intercepts such an operation subsequently, it notifies the coordinating monitor, which in turn notifies $M_d$. Otherwise, $M_d$ proceeds with matching of $pat$ as it normally would, but when it finishes the match (or determines that the current partial match for $pat$ has failed), it will send a message to the coordinating monitor indicating the end of the atomic sequence. Given this implementation of atomic sequence matching, the considerations in pattern-matching are (a) detect the beginning of a match for $pat$, and (b) detect when a match for a prefix of $pat$ can either be extended into a complete match, or when partial match cannot be extended. It is easy to accomplish these in the context of our NEFA construction algorithm. In particular, since each state of the NEFA identifies the set of positions within the REE that may correspond to the state, it is possible to detect when we begin a match for a pattern, when we complete it and when a (partial) match for the pattern fails. Given the patterns $begin(); (a(x, y) || b); (a || b); b(x)$ and $begin(); (a(y, z) || c(y)); (a || c); c(y)$, the preprocessing steps result in the following pattern:

$$\begin{align*}
(a^1(\&1 = \&2)/t_1 := \&1; (a^2[\&3]); b^4/\&1 = t_1; \#^5 \parallel \\
(a^6/\&2 = \&3) [c^7]/t_2 := \&1; (a^8[\&9]); c^{10}/\&1 = t_2; \#^{11}
\end{align*}$$

The purpose of numbering the REE positions is similar to that of earlier algorithms for constructing DFA from regular expressions [Aho90, MY60]. Each state $S$ in the NEFA is associated with a subset $P_S$ of positions in the REE as follows: a position $p \in P_S$ iff there is a path from the start state of the NEFA to $S$ that matches the prefix of the REE up to position $p$. Intuitively, $P_S$ denotes that set of positions in the REE up to which we may have matched an input event history that took the NEFA from its start state to the state $S$.

The construction of the NEFA for an REE $R$ begins with the start state $S_0$. The positions $P_{S_0}$ associated with the start state is defined as $first(R)$, where $first$ is the function defined below.

$$\begin{align*}
\text{first}(e|C) &= \{\text{pos}(e|C)\} \\
\text{first}(A; B) &= \text{first}(A) \cup \text{first}(B) \quad \text{if empty}(A) \\
\text{first}(A; B) &= \text{first}(A) \quad \text{otherwise} \\
\text{first}(A|B) &= \text{first}(A) \cup \text{first}(B) \\
\text{first}(A*) &= \text{first}(A)
\end{align*}$$

Here, the notation $\text{pos}(e)$ denotes the position number associated with the event $e$. $\text{empty}(A)$ is true if $A$ matches the empty string. The \textbf{first} of the example REE would be the positions \{1, 6, 7\}.

The next step in construction is to identify the distinct events $e_1, \ldots, e_m$ that occur at one of the positions in $P_S$. We create $m$ transitions from $S$ to \textit{dummy} states as shown with dotted lines in Figure 4. The set of positions $P_{S_j}$ associated with each of these dummy states is given by:

$$P_{S_j} = \{j \in P_S|e_i \text{ occurs at } j \text{ in } R\}$$

Let $C_j$ be the condition associated with a position $j \in P_{S_i}$. As the next step, we select a $k \in P_{S_i}$ and create two transitions from $S_i$ to dummy states $S_{ii}$ and $S_{i2}$, the first one to be taken when $C_k$ holds and the second to be taken otherwise. $P_{S_{i2}}$ and $P_{S_{ii}}$ are given by:
Figure 5: NEFA (also a DEFA) for the example patterns

C1: $S_1 = S_2$  A1: $t_1 := S_1$
C2: $S_2 = 3$  A2: $t_2 := S_2$

Figure 4: Partially constructed NEFA

$$P_{S_{i1}} = \{ j \in P_{S_i} | C_j \land C_k \neq \text{false} \}$$
$$P_{S_{ia}} = \{ j \in P_{S_i} | C_j \land \neg C_k \neq \text{false} \}$$

We note that the correctness of the algorithm is not affected even if we included a $j$ in $P_{S_{i1}}$, such that $C_j \land C_k = \text{false}$, but the size of NEFA may increase. A similar comment applies to $P_{S_{ia}}$ as well. (The size increase will likely be reversed by an optimizing compiler that may be used to compile the code generated by our NEFA construction algorithm.) This process is repeated at $S_{i1}$ and $S_{ia}$ recursively, while ensuring that each condition is tested at most once. The dashed lines in Figure 4 show these transitions.

When we reach a dummy state $S_d$ where all conditions have been tested, we are ready to create new (non-dummy) states. Let $A_1, \ldots, A_r$ be the distinct assignment actions among the positions $P_{S_d}$. Then we create a total of $r$ states $S'_{1}, \ldots, S'_{r}$, with $\epsilon$-transitions from $S_d$ to $S'_{1}$ labeled with assignments actions $A_n$, for $1 \leq n \leq r$. $P_{S_d}$ is determined as follows. Let $P_s$ denote the subset of $P_{S_d}$ where assignment actions $A_k$ appear. Then $P_{S'_d} = \bigcup_{p \in P_s} \text{follow}(p)$, where follow is defined by:

$$\text{follow}(p) \supseteq \text{first}(Q), \text{if } p \text{ is rightmost for a subexpression } Q \ast \text{ in } R$$

$$\text{follow}(p) \supseteq \text{first}(Q), \text{if } p \text{ is rightmost within } Q' \text{ where } Q' \text{ is in } R$$

$$\text{follow}(p) \supseteq \text{follow}(q), \text{if } p \text{ and } q \text{ are rightmost in } Q \text{ and } Q', \text{where } Q \parallel Q' \text{ is in } R,$$

or $Q, Q'$ is in $R$ and $\text{empty}(Q')$

A position $p'$ is included in follow(p) only if required by the conditions above. We can reduce the number of $\epsilon$ transitions by merging states corresponding to assignments from different patterns. (The set of positions of the merged state is given by the union of these sets for the original states.) We can also avoid creating a new (non-dummy) state $S'_n$ if $P_{S'_n}$ is identical to $P_{S'm}$ for another state $S''$ in the NEFA. Figure 4 shows the partially constructed NEFA after these optimizations.

A formal algorithm that captures the description given so far, as well as the continuation of the algorithm to build the complete NEFA is shown below.

```plaintext
procedure NEFAGen(REE $R$)
Create start state $S_0$
$pos[S_0] := \text{first}(R)$; $\text{StateSet} := \{ S_0 \}$
foreach $S \in \text{StateSet}$ do
    foreach event $e$ occurring at some $p \in pos[S]$ do
        create a state $S'$
```


\[ \text{pos}[S'] := \{ j \in \text{pos}[S] \mid C_j \text{ occurs at } j \text{ in } R \} \]

\texttt{BuildDummyTrans}(R, S', \text{StateSet}, \phi, \phi) 

end 

end 

procedure \texttt{BuildDummyTrans}(REE, R, State S',
Set of State StateSet,
Set of Condition PosCond, NegCond) 

if \exists \text{ condition } C \text{ at some } p \in \text{pos}[S'] 
\text{ s.t. } C \notin \text{PosCond} \cup \text{NegCond} 
create state \text{S}_1 
\text{pos}[S_1] := \{ j \in \text{pos}[S'] \mid C_j \land C \neq \text{false} \}, 
\text{where } C_j \text{ is the condition at position } j 
\texttt{BuildDummyTrans}(R, S_1, \text{StateSet},
PosCond \cup \{ C \}, \text{NegCond}) 
create state \text{S}_2 
\text{pos}[S_2] := \{ j \in \text{pos}[S'] \mid C_j \land \neg C \neq \text{false} \},
\text{where } C_j \text{ is the condition at position } j 
\texttt{BuildDummyTrans}(R, S_2, \text{StateSet}, PosCond,
NegCond \cup \{ C \})

else /* all conditions tested */
\text{P} := \text{pos}[S']; \text{P}' := \phi; \text{Asgs} := \phi; 
while \exists p \in \text{P} do 
if \text{A}_p \text{ does not conflict with Asgs}
\text{P}' := \text{P}' \cup \{ p \}; \text{P} := \text{P} \setminus \{ p \};
\text{Asgs} := \text{Asgs} \cup \text{A}_p;
end 
if ( (\text{P} = \phi) \lor (\text{A}_p \text{ conflicts with Asgs}) )
/* create a new state */
if \neg \exists \text{S} \in \text{StateSet} \text{ with } \text{pos}[\text{S}] = \text{follow}(\text{P}') 
create a new state \text{S},
set \text{pos}[\text{S}] := \text{follow}(\text{P}')
\text{StateSet} := \text{StateSet} \cup \{ \text{S} \}
end 
create \varepsilon\text{-transition from } \text{S}' \text{ to } \text{S}
associated with actions \text{Asgs}
\text{P}' := \phi; \text{Asgs} := \phi
end 

end 

end 

After the above algorithm, a post-processing phase merges sequences of dummy transitions into a single one, eliminating the dummy states in the process. It also marks any state that includes an end-marker position as a final state for the corresponding pattern. The NEFA constructed by the algorithm for our example is shown in Figure 5.

**Theorem 3** \texttt{NEFAgen}(R) builds a NEFA to recognize R

For the proof, we first need to formalize the notion of a prefix of an REE up to a position \( p \). Then we show that given any event history \( H \), it matches a prefix of \( R \) up to a position \( p \) iff there is a path in the NEFA traversable on \( H \) to reach a state \( S \) such that \( P_S \) includes \( p \). This step is proved by induction on the length of \( H \). The correctness of the theorem follows readily from this result.

### 5.2 Post Processing

After constructing the NEFA as described above, we can eliminate the dummy states as follows. Replace each path between two non-dummy states that consists only of dummy states by a single edge. This edge is associated with a condition that is the conjunction of all the conditions on the replaced path, plus the assignments made on the path. While this process of elimination does not necessarily contribute to reducing the size of the NEFA, it is useful in reducing the clutter in illustrations and discussions.

A second step in post-processing is to identify the final states. Any state in the NEFA that contains a position that corresponds to an end-marker of a pattern \( i \) is an accepting state for that pattern. We attach the reaction components of the matched patterns to these states.

### 5.3 Properties of NEFA

**Matching REE(0) Patterns** As mentioned earlier, \texttt{REE}(0) patterns are those that cannot refer to values of event arguments except immediately after the occurrence of the event. With our variable renaming scheme in place, this means that there would be no need for state variables, and hence no assignments. Note that the only source of non-determinism in the above algorithm arises because of \( \varepsilon \)-transitions associated with each group of assignments. Therefore, the NEFA generated is a DFA for \texttt{REE}(0) languages.

**Size** It is easy to see from our algorithm that after the elimination of dummy states, there can be at most \( 2^n \) states, where \( n \) is the size of the \texttt{REE}, because each state is associated with a subset of positions in the \texttt{REE}, and there are at most \( 2^n \) such subsets. It is interesting to note that although we are dealing with a more complex language than \texttt{REE}, our bound for number of states is the same as the corresponding bound for \texttt{RE}.

To get an idea of the size of the NEFA, one has to consider the states as well as the space required for storing the transitions. If \( \text{maxp} \) denotes the maximum number of positions where the same event occurs in an \texttt{REE}, then we can show that the total
space requirements for the states and transitions is bounded by \( O(2^{n \cdot \text{max}}) \).

We note that as in the case of the DFA construction algorithm from [Aho00], the worst case space requirements do not reflect the space usage in practice. Often, as in the case of the above example and in the examples studied Section 6.3, we end up with an automaton whose size is much smaller than the exponential upper bound.

5.4 Further Optimization of NEFA

The NEFA generated by our algorithm can be further optimized using a number of techniques. For instance, some combinations of conditions examined along some paths in the automaton may not be possible, and it is useful to prune out those branches. Similarly, several conditions may overlap, so the simple approach of checking the conditions in sequence may be inefficient. We do not try to address these optimization problems in our algorithm. Instead, the output of our translation algorithm is a C++ class to simulate a NEFA. The compiler for C++ can routinely perform these optimizations — the first of the above opportunities for optimization is identified by dead-code elimination, and second identified by common subexpression elimination. Relying on the compiler’s array of powerful optimization techniques is, in our belief, the best approach to address a variety of optimization issues in this context.

A second class of optimization opportunities exist that is not realized by C++ compiler optimizations. This concerns the fact that even after the abovementioned optimizations, we may have many transitions out of a NEFA node with different conditions. In the worst case, identification of the applicable transition(s) may require time proportional to the out-degree of an automata node. The out-degree, in some instances, can be proportional to the number of patterns, and in those cases, our pattern-matching algorithm becomes sensitive to the number of patterns. We are currently investigating techniques to improve the performance of our algorithm for such patterns.

6 Implementation and Performance

Our system implementation consists of a compiler and a runtime system.

6.1 Compiler

The front-end of the compiler is responsible for parsing a specification. Its implementation is routine, based on standard compiler construction tools Flex and Bison. Type-checking is performed subsequently, and then we translate the pattern components of our specifications into a NEFA using the algorithm described in the previous section. The reaction components of the rules are attached to those states that accept the pattern corresponding to the respective rules.

The NEFA is then translated into a C++ class NEFA as follows. At runtime, the state of the NEFA is maintained in a structure called FSM, which stores the control state as well as the state variables. To support nondeterminism, multiple instances of an FSM can be created using a clone function that makes identical copies of an FSM. At any point during runtime, zero, one or more copies of the FSM may be active.

The transitions of the NEFA are captured in the C++ code as follows. A system call sc is delivered to the NEFA class by invocation of a member function on the NEFA class with the name sc. The code for this class sequences through the list of FSM’s that are currently active. For each FSM, its state is updated to reflect a transition that it would have made on the event sc and its arguments. If multiple transitions are possible, suitable number of copies of the FSM are made and each copy follows one of the transitions.

6.2 Runtime System

The output code produced by the compiler is linked with a runtime support system that provides the infrastructure for intercepting and delivering system calls to the detection engine. Events are delivered by invoking the corresponding member functions on the NEFA class described above. The runtime system also provides the functions needed by the detection engine to alter the behavior of system calls. With regard to data access, it provides support functions to access system call argument values.

The runtime system has been implemented by modifying the system call interface within the operating system. We do not provide a detailed explanation of the runtime system here, as our focus in this paper is on efficient runtime matching operations. Our results indicate that the overhead for interception is more or less constant per system call, and adds
about 30% overhead to the cost of a system call [Bov99]. However, since system calls account for only a small fraction of the CPU time used by an application, the increase in CPU time (counted as the sum of user and system time on UNIX) is in the range of 2% to 30%. Most applications, especially those that are CPU-intensive, impose overheads at the bottom of this range, while I/O intensive applications such as tar and ftp lead to higher overheads. [FBF99], which employs an interception mechanism similar to ours, reports overheads in the range of 2% to 4% for a different set of applications, namely, gcc and httpd.

We remark that the overheads for system call interception are nontrivial. In fact, our fast matching algorithm reduces the checking time to the point where system call interception accounts for the dominant portion of the total overhead imposed by our approach. In particular, this implies that there is no penalty to be paid due to the use of sophisticated pattern-matching based approach such as ours over a method that uses simpler rules that are triggered on individual system calls.

### 6.3 Performance Results

We studied the performance of our system with three server programs, namely, ftpd, telnetd and httpd. The specification for ftpd was as described earlier. The specification for telnetd and httpd are not shown, but are comparable in size and complexity to that of ftpd. Our experiments were designed to evaluate the runtime overhead for monitoring (both in terms of time and space) and the size of the pattern-matching automata. In measuring the runtime overhead, we omitted the cost of intercepting system calls since our primary interest is in evaluating the performance of the pattern-matching algorithms. Moreover, the overhead for interception remains essentially a constant independent of the size of the patterns or length of the execution trace.

#### 6.3.1 Timing Results

The best indicator of runtime overheads is the fact that only two of all the patterns in these three specifications were non-REE(0). This implies that the runtime matching effort consists of performing a transition on a deterministic automaton, which is about as expensive as the cost of the tests involved in the applicable transitions. As compared to the cost of a typical system call, this is indeed very low. The non-REE(0) patterns did not contribute much to the overhead either, as they led to very few cases where cloning was required. Specifically, the number of clones active at any point at runtime was less than five, with each FSM requiring several bytes of storage. Thus the runtime storage requirements were very low. Simulating such a small number of FSMs at runtime is also very fast.

Figure 6 summarizes our experimental results. They show that in fact, the overheads due to the monitoring are almost imperceptible for all three applications. The runtime data storage requirements for the FSM are too small to be measured.

Figure 7 shows the overhead for matching each system call, averaged across the three programs. The graph indicates a slight increase with increase in number of system calls, which is to be expected due to the presence of non-REE(0) rules. However, the rate of increase is extremely small, at about 5% when the number of system calls has increased by about 2000%. Thus, it is meaningful to talk about overhead as a percentage of the total runtime, as was done in Table 6. To obtain the results in the table and the figure, we exercised the three programs with a random combinations of valid commands of different lengths.

<table>
<thead>
<tr>
<th>TestCase</th>
<th>Time to run(s)</th>
<th>Time to match all sysCalls (s)</th>
<th>Overhead</th>
</tr>
</thead>
<tbody>
<tr>
<td>ftpd</td>
<td>2.2s</td>
<td>0.03</td>
<td>1.5%</td>
</tr>
<tr>
<td>telnetd</td>
<td>3.1s</td>
<td>0.04</td>
<td>1.3%</td>
</tr>
<tr>
<td>httpd</td>
<td>5.8</td>
<td>0.09</td>
<td>1.5%</td>
</tr>
</tbody>
</table>

Figure 6: Overhead due to system call pattern matching. Results taken on 350MHz Pentium II Linux PC with 128MB memory and 8GB IDE disk.

Figure 7: Matching time per system call (in microseconds).
6.3.2 Automaton Size

To evaluate the increase in size of the automaton when the number or complexity of patterns is increased, we have plotted the automaton size as a function of the size of the patterns. The size measure we use is given by the number of REE events and operators, taken over all of the patterns. Event arguments and conditions are not included, as the number of states is not very much affected by their presence or absence. The REE patterns of interest were those corresponding to our three benchmark programs, ftpd, telnetd and httpd. Randomly chosen subsets of these patterns were compiled and the corresponding size of the automaton was identified. These were then plotted as shown in Figure 8.

Although the worst-case size is exponential in the size of REE, we find that in practice, the size increases more or less linearly with the total REE size. Although it is not readily apparent from the graph, the rate of increase in number of states tended to decrease after a while in our examples. The reason for this is that existing patterns had already created many states that could already represent some of the stages of matching the new pattern, and thus only fewer additional states were required. However, we must exercise caution in generalizing from these three programs, as the relationship between pattern size and automaton size can vary greatly from one set of patterns to another.

7 Related Work

7.1 Intrusion Detection

Techniques for prevention of intrusions draw on previous research on (post-attack) intrusion detec-

tion. Intrusion detection techniques can be broadly divided into misuse detection [PK92, Kumar95], anomaly detection [ALJTV95, FHS97, GSS99], and specification-based detection [Ko96, SBS99].

Among misuse-based approaches, a state-transition diagram based approach is used in [PK92] to capture signatures of intrusions. [Kumar95] uses colored petri nets to specify intrusive activity. This language is more expressive than ours in some ways (e.g., ability to capture occurrence of two concurrent sequences of actions), and less expressive in some other ways (e.g., ability to capture atomic sequences or the occurrence of one event immediately following another). Nevertheless, most intrusion signatures expressed in [Kumar95] can be easily captured in our language as well and hence our compilation techniques are applicable to their approach.

Among anomaly detection approaches, one of the first works based on program behaviors (as opposed to user behaviors) was that of [FHS97]. Recently, these results have been improved by [SBS99] using a neural network based approach that produces very accurate anomaly detectors. All these approaches deal only with system call names, not with arguments. This simplifies the problem of learning normal behaviors of processes, which is the main focus of their work. However, for intrusion prevention or confinement, argument values are indeed important, e.g., we cannot otherwise distinguish an action to write a log file from one to modify the /etc/passwd file. Thus a language like REE is more appropriate in this context.

A specification-based approach achieves the accuracy of misuse detection, while addressing one of its deficiencies, namely, the inability to deal with unknown intrusions. It was first proposed in [Ko96]. They use a pattern language based on context-free grammars extended with variables, and formulate the intrusion detection problem as one of parsing the audit logs with respect to these grammars. In contrast, our language is based on an extension of regular languages with variables. While context-free languages are more expressive than regular languages, this is not necessarily true when variables have been added to these languages. On the other hand, a regular language based formulation lends itself more readily to an automaton based pattern-matching approach that can be implemented efficiently.

![Figure 8. Increase in automaton size with specification size.](image-url)
7.2 Preventive Approaches

Some of the preventive approaches are based on protecting systems against underlying software errors that are exploited by attackers. For instance, malicious code detection techniques [BD96, GM96, LL95] employ program analysis techniques to detect security-related errors in the source code. Similarly, [CPMWBGWZ98] developed compilation techniques that add additional checks into the code generated to identify (and prevent) attacks that exploit buffer overflows to overwrite the return addresses stored on a process stack.

Interception of system calls, followed by interception of arbitrary code at this point, has been proposed by many researchers as a way to confine applications. The Janus system [GWTB96] incorporates a user-level implementation of system call interception. It is aimed at confining helper applications (such as those launched by web-browsers) so that they are restricted in their use of system calls. Their language is tailored to restrict individual system calls without any regard for the context in which they appear. This approach is well-suited for fine-grained access control and sandboxing. The kernel hypervisor [MLO97] approach is similar to the Janus approach, but is implemented within an operating system kernel using loadable modules. A more comprehensive set of system call interception capabilities was developed in [GPRA98]. Their approach is geared for a broad range of applications aimed at augmenting software functionality in areas such as security and fault-tolerance. [FBF99] focuses on the related problem of developing languages customized for writing interception code (also known as wrapper code), and runtime infrastructure for their installation and management. Unlike the preceding approaches, their language can more easily capture sequencing relationships among system calls. But they do not focus on pattern-based techniques for intrusion detection. Moreover, computational issues in efficient matching of system call sequences are not addressed.

7.3 Other Work

There has been a significant amount of earlier work in regular languages and pattern-matching. In particular, our algorithm for NEFA construction is based in part on some of the ideas developed in [Aho90, MY60] for direct construction of DFA from regular expressions. The language design has been influenced by previous pattern-matching based languages such as Lex and Awk [AWK88].

Regular languages and \( \omega \)-regular languages, together with their automata-equivalents, have been used widely in formal specification and verification of concurrent systems [Kruschul94]. In the related area of program analysis, [OO90] develops an approach for expressing sequencing constraints as regular expressions, and compile-time checking of these constraints using dataflow analysis. [Schneider98] proposes to use Buchi automata for monitoring security properties of programs. The main difference between these works and ours is that they operate on regular or \( \omega \)-regular languages, while our language is REE. Another difference is that in the context of verification, we are often interested in properties of infinite sequences, while our interest is in behaviors that manifest themselves in finite sequences.

Describing properties of event sequences is an important problem in building, prototyping, debugging and monitoring distributed systems. Languages such as CSP [Hoare78], LOTOS [BB89] and Esterel [BCG87] support very simple patterns, permitting no sequencing or closure operators. The task sequencing language developed in [LHMBH87] and its successor Rapide [LV95] support an expressive pattern language that is significantly more expressive than ours. Moreover, they support a partially ordered set model of event histories, whereas our current model is a linear sequence. However, it is not clear whether this language is amenable to efficient pattern matching at runtime, as they do not address the problem of automata-based techniques for efficient matching of these patterns.

8 Conclusions

In this paper we presented an approach for building survivable systems. Our approach is based on monitoring system calls made by processes (running on the system to be protected), comparing them against patterns characterizing normal (or abnormal) system call sequences, and initiating appropriate responses when (potentially) damaging system calls are made. These responses may preempt intrusion, or otherwise isolate and contain any damage. Since attacks can be prevented and/or contained, our approach can satisfactorily address intrusions arising due to software errors in otherwise trustworthy programs, as well as malicious programs (e.g., Trojan horses) from untrusted sources. Moreover, when new vulnerabilities (not protected by existing specifications) are identified, we can protect against them using appropriate specifications, instead of
disabling vulnerable software until the vendor provides a patch.

One of the main challenges in making our approach practical is the ability to perform runtime prevention/detection that is fast enough to be included as part of processing every system call made by every process. We proposed a solution to this problem in this paper by developing an algorithm that compiles patterns in our specification language into an extended finite state automaton (EFSA). Our implementation results demonstrate that detection can be performed very fast (1 to 2% overhead to program execution time) using our approach. A key benefit of our approach (supported by theory as well as performance experiments) is that the detection time is insensitive to the complexity or number of patterns used in the specification. In most cases, our algorithm takes a constant time per system call intercepted, and uses a constant amount of storage. These advantages make our algorithm attractive for any other approach that performs intrusion detection by some form of pattern-matching.

We showed that for a class of patterns (specifically, REE(0) patterns) the automata constructed by our algorithm are deterministic. A more precise characterization is desirable, since in practice, our algorithm builds deterministic automata for a much larger class of patterns. Such a characterization may also enable us to restrict the specification language so that patterns that can lead to very large overheads (associated with simulating a large number of NEFA instances at runtime) can be avoided. Another area of future research is the application of techniques presented in this paper to the larger problems of distributed system monitoring and software debugging.

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