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Pāṇini: Reconciling Concurrency and Modularity in Design

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Abstract
Writing correct and efficient concurrent programs still remains a challenge. Explicit concurrency is difficult, error prone, and creates code which is hard to maintain and debug. This type of concurrency also treats modular program design and concurrency as separate goals, where modularity often suffers. To solve these problems, we are designing a new language that we call Pāṇini. In this paper, we focus on Pāṇini’s asynchronous, typed events which reconcile the modularity goal promoted by the implicit invocation design style with the concurrency goal of exposing potential concurrency between the execution of subjects and observers. Since modularity is improved and concurrency is implicit in Pāṇini, programs are easy to reason about and maintain. Furthermore, races and deadlocks are avoided entirely yielding programs with a guaranteed sequential semantics. To evaluate our language design and implementation we show several examples of its usage as well as an empirical study of program performance. We found that not only is developing and understanding Pāṇini programs significantly easier compared to standard concurrent object-oriented programs, but performance of Pāṇini programs is comparable to the equivalent programs written using Java’s fork-join framework.

1. Introduction

Coming together is a beginning. – Henry Ford

The idea behind Pāṇini’s design is that if programmers structure their system to improve modularity in its design, they should get concurrency for free.

1.1 Object-oriented (OO) Concurrency Features
It is widely accepted that multicore computing is becoming the norm. However, writing correct and efficient concurrent programs using concurrency-unsafe features remains a challenge [5, 33–35, 46]. A language feature is concurrency-unsafe if its usage may give rise to program execution sequences containing two or more memory accesses to the same location that are not ordered by a happens-before relation [25]. Several such language features exist in common language libraries, for example, threads, processes, Futures, and FutureTask are all examples from the Java programming language’s standard library [35,46]. Using such language features has advantages, e.g. they can encapsulate complex synchronization code and allow its reuse.

To illustrate, consider the implementation of a genetic algorithm in Java presented in Figure 1. The idea behind a genetic algorithm is to mimic the process of natural selection. Genetic algorithms are computationally intensive and are useful for many optimization problems [41]. The main concept is that searching for a desirable state is done by combining two parent states instead of modifying a single state [41]. An initial generation with n members is given to the algorithm. Next, a cross over function is used to combine different members of the generation in order to develop the next generation (lines 10–16 in Figure 1). Optionally, members of the offspring may randomly be mutated slightly (lines 18–23 in Figure 1). Finally, members of the generation (or an entire generation) are ranked using a fitness function (lines 25–29 in Figure 1).

Multiple Concerns of the Genetic Algorithm. In the OO implementation of the genetic algorithm in Figure 1 there are three concerns standard to the genetic algorithm: cross over (creating a new generation), mutation (random changes to children), and fitness calculation (how good is the new generation). Logging of each generation is another concern added here since it may be desirable to observe the space searched by the algorithm (lines 17 and 24). The final concern here is concurrency (lines 4, 7—9, and 30—33). In this example, production of a generation is run as a FutureTask. The different shading shows code corresponding to each concern as illustrated in the legend.

1.2 Problems with Explicit Concurrency Features
Explicit concurrency. With explicit concurrency, the programmer must divide the program into independent tasks.

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1 Original proposal for futures in MultiLisp is concurrency-safe [40].
Separation of modular and concurrent design. Another significant shortcoming of these language features, or perhaps the discipline that they promote, is that they treat modular program design and concurrent program design as two separate and orthogonal goals.

From a quick glance at Figure 1, it is quite clear that the five concerns are tangled. For example, the code for concurrency (lines 4, 7-9, and 30-33) is interleaved with the logic of the algorithm (the other four concerns). Also, the code for logging occurs in two separate places (lines 17 and 24). This arises from implementing a standard well understood sequential approach and then afterward attempting to expose concurrency rather than pursuing modularity and con-
In Païn̄ini, an event type is seen as a decoupling mechanism that is used to interface two sets of modules, so that they can be independent of each other. Below we briefly describe the syntax in the context of the genetic algorithm implementation in Païn̄ini shown in Figure 2 (a more detailed description appears in Section 2). In the listing we have omitted initializations of classes for brevity. In this listing an example of an event type appears on lines 1–3, whose name is GenAvailable and that declares to make one context g of type Generation available.

Certain classes, to which we refer to as subjects from here onward, declaratively and explicitly announce events. The class CrossOver (lines 4–19) is an example of such a subject. This class contains a probability for the cross over operation and a maximum depth at which the algorithm will quit producing offspring. The method cross for this class computes the new generation based on the current generation (lines 11–19). After the cross method creates a new generation, it announces an event of type GenAvailable (line 18) denoted by code announce GenAvailable(gl).

Another set of classes, which we refer to as observers from here onward, can provide methods, called handlers that are invoked (implicitly and potentially concurrently) when events are announced. The listing in Figure 2 has several examples of observers: CrossOver, Mutation, Logger and Fittest. A class can act as both subject and observer. For example, the classes CrossOver and Mutation are both subjects and observers for events of type GenAvailable.

In Païn̄ini classes statically express (potential) interest in an event by providing a binding declaration. For example, the Mutate concern (lines 20–35) wants to randomly mutate some of the population after it is created. So in the implementation of class Mutatation there is a binding declaration (line 26) that says to run the method mutate (lines 27–35) when events of type GenAvailable are announced.

At runtime these interests in events can be made concrete using the register expressions. The class Mutatation has a method on lines 22–25 that when called registers the current instance this to listen for events. After registration, when any event of type GenAvailable is announced the method mutate (lines 27–35) will run with the registered instance this as the receiver object.

Similarly, the method logit (line 39) will log each generation when events of type GenAvailable are announced. Finally, method check in class Fittest (lines 41–51) will determine the better fitness between the announced generation and the previously optimal generation when events of type GenAvailable are announced.

Benefits of Païn̄ini’s Version of Genetic Algorithm. At a quick glance, we can see from the shading that the four remaining concerns are no longer tangled and they are separated into individual modules. This separation not only makes reasoning about their behavior simple but also allows us to expose potential concurrency between them.

Furthermore, the concurrency concern has been removed entirely since the implementation of Païn̄ini encapsulates all the code for managing the concurrency. By not requiring users to write this code, Païn̄ini avoids any threat of incorrect or non-deterministic concurrency thus easing the burden on the programmer. This allows the programmer to focus on creating a good, maintainable modular design.

Finally, additional concurrency between these four modules is now automatically exposed. Thus, Païn̄ini reconciles modular program design and concurrent program design.

Advantages of Païn̄ini’s Design over Related Ideas. Païn̄ini is most similar to our previous work on Ptolemy [36], but Païn̄ini’s event types also have concurrency advantages.

It is also similar to implicit invocation (II) languages [12, 31] that also see events as a decoupling mechanism. The advantage of using Païn̄ini over an II language is that asynchronous, typed events in Païn̄ini allow developers to take advantage of the decoupling of subjects and observers to expose potential concurrency between their execution.

These events also relieve programmers from the burden of explicitly creating and maintaining threads, managing locks, and shared memory. Thus Païn̄ini avoids the burden of reasoning about the usage of locks, which has several benefits. First, incorrect use of locks may have safety problems. Second, locks may degrade performance since acquiring and releasing a lock has overhead. Third, threads are cooperatively managed by the language runtime, thus thrashing due to excessive threading is avoided. These benefits make Païn̄ini an interesting point in the design space of concurrent languages.

In summary, this work makes the following contributions:

1. Païn̄ini’s language design with a simple and flexible implicit concurrency model such that Païn̄ini programs are
   • free of data races,
   • free of deadlocks, and
   • have a guaranteed sequential semantics;

2. a precise operational semantics and type system for Païn̄ini’s novel constructs;

3. an efficient implementation of Païn̄ini’s design as an extension of the JastAdd compiler [13] that relies on:
   • a sound algorithm for finding inter-handler dependence at registration time to maximize concurrency,
   • a simple and efficient algorithm for scheduling concurrent tasks that builds on the fork/join framework [27];

4. a detailed analysis of Païn̄ini and closely related ideas;

5. and, an empirical performance analysis of Païn̄ini implementations using canonical concurrency examples.
1.4 Organization of this Paper

We have developed a small-step operational semantics, a type system and proved its soundness. Their details can be found in Appendix A (on page 14) and Appendix B (on page 16). The next section describes the design and implementation of the Pa\-\text{nini} language in detail. So a theoretical roadmap might be to read the next section followed by Appendix A and Appendix B.

Section 3 gives more examples in Pa\-\text{nini} and describes our performance evaluation and experimental results. So an alternative roadmap might be to read the next two sections.

Finally, Section 4 surveys related work, and Section 5 describes future directions and concludes.

2. Pa\-\text{nini}'s Design

\textit{Pa\-\text{nini}, fl. c.400 BC, Indian grammarian, known for his formulation of the Sanskrit grammar rules, the earliest work on linguistics.}

In this section, we describe Pa\-\text{nini}'s design. Pa\-\text{nini}'s design builds on our previous work on the Ptolemy [36] and Eos [38] languages as well as implicitly parallel languages such as Jade [39]. Pa\-\text{nini} achieves concurrent speedup by executing handler tasks (that do not conflict) concurrently. The novel features of Pa\-\text{nini} are found in its concurrency model, event model and type system.

2.1 Pa\-\text{nini}'s Abstract Syntax

\begin{verbatim}
prog ::= decl* e
decl ::= class c extends d ( | field | meth | binding |
| event p | form |
field ::= c f;
meth ::= c m ( form | event )
t ::= void binding ::= when p do m;
form ::= c var, where var is this
e ::= new c () | var * e | e.m ( c ) | e.f | e.f = e | cast e e |
| form = e | e | e [ register | announce | GenAvailable ]

where
c, d ∈ C, the set of class names
p ∈ P, the set of event type names
f ∈ F, the set of field names
m ∈ M, the set of method names
var ∈ \{this\} \cup \{x\}, V is the set of variable names
\end{verbatim}

\textbf{Figure 3}. Pa\-\text{nini}'s abstract syntax, based on [9,36].

Pa\-\text{nini} features new mechanisms for declaring events and for announcing these events. These features are inspired by implicit invocation (II) languages such as Rapide [12] and concurrent languages such as Erlang [4]. The technical presentation of Pa\-\text{nini} builds upon previous object-oriented calculi [9,10,18,22,36]. The object-oriented part of Pa\-\text{nini} has classes, objects, inheritance, and subtyping, but it does not have \textit{super}, interfaces, exception handling, built-in value types, privacy modifiers, or abstract methods.

The abstract syntax is shown in Figure 3. A Pa\-\text{nini} program consists of a sequence of declarations followed by an expression, which can be thought of as the body of a “main” method. In this syntax, the novel features are: event type declarations (\textit{event}), event announcement expressions (\textit{announce}), and handler registration expressions (\textit{register}). Since Pa\-\text{nini} is an implicitly concurrent language, it does not feature any construct for spawning threads or for mutually exclusive access to shared memory. Rather, concurrent execution is facilitated by announcing events, using the \textit{announce} expression, which may cause handlers to run concurrently. Examples of the syntax can be seen in Figure 2. This example is described thoroughly in Section 1.3.

\textbf{Top-level Declarations}. The two top-level declaration forms, classes and event type declarations, may not be nested. A class has exactly one superclass and may declare several fields, methods, and bindings. An event type (\textit{event}) declaration has a name (p), and zero or more context variable declarations (\textit{form*}). These context declarations specify the types and names of reflective information exposed by conforming events. An example is given in Figure 2 on lines 1-3 where \textit{event GenAvailable} has one context variable \textit{Generation g} which denotes the generation which is now available. The intention of this event type declaration is to provide a named abstraction for a set of events that result from a generation being ready.

Like Eos [37] classes in Pa\-\text{nini} may also contain binding declarations. A binding declaration mainly consists of two parts: an event type name and a method name. For example, in Figure 2 on line 10 the class \textit{CrossOver} declares a binding such that the \textit{cross} method is invoked whenever an \textit{event} of type \textit{GenAvailable} is announced. This method may run asynchronously with other handler methods.

2.2 Pa\-\text{nini}'s Expressions

\textbf{Registration}. Like II languages, a module in Pa\-\text{nini} can express interest in events, e.g. to implement the observer design pattern [19]. Just like II languages, where one has to write an expression for registering a handler with each event in a set. and similar to Ptolemy [36], such modules are defined using a binding inside a class declaration. Examples are shown on lines 7, 23, 38 and 44 in Figure 2.

\textbf{Object-oriented Expressions}. The formally specified version of Pa\-\text{nini} is an expression language, thus the syntax for expressions includes several standard OO expressions [9,10,36]. These OO expressions include construction of an object (\textit{new c ()}), variable dereference (\textit{var}, including \textit{this}), field dereference (\textit{.f}), \textit{null}, cast (\textit{cast t e}), assignment to a field (\textit{e.f = e}), a definition block (\textit{t var = e1; e2}), and sequencing (\textit{e1; e2}). Their semantics and typings are fairly standard [9,10]. In the examples, we use conditionals of the form

\begin{verbatim}
if e == null { e } else { e }
\end{verbatim}

and

\begin{verbatim}
for e ; e ; e { e }, which has standard desugaring.
\end{verbatim}

\textbf{Concurrency in Pa\-\text{nini}}. The \textit{announce} expression enables concurrency in Pa\-\text{nini}. The expression \textit{announce p} \textit{(e*)} \textit{; e} signals an event of type \textit{p}, which may run any handler bodies that are applicable to \textit{p} asynchronously, and waits
for the handlers to finish. In Figure 2 the body of the cross method contains an announce expression on line 18. On evaluation of the announce expression, Pānini first looks for any applicable handlers. Here, the handlers CrossOver, Mutation, Logger, and Fittest, are declared to handle the events of type GenAvailable. Such handlers may run concurrently, depending on whether they interfere with each other.\(^2\) The type system of Pānini computes the potential effects of the handlers.\(^3\) Pānini will update the handlers’ potential effects when they register and decide which handlers conflict between each other. Pānini then schedules the handlers to maximize concurrency while maintaining the sequential semantics. The evaluation of the announce expression then continues with evaluating the sequence on line 18, which returns from the method. The announcement of the event allows for potential concurrent execution of the bodies of the cross (lines 11–19), mutate (lines 27–35), logit (line 38), and check (lines 45–51) methods.

The announce expression also binds values to the event type declaration’s context variables. For example, when announcing event GenAvailable on line 18, g1 is bound to the context variable g on line 2. This binding makes the new generation available in the context variable g, which is needed by the context declared for the event type GenAvailable.

### 2.3 Pānini’s Registration Algorithm

**Handlers’ abstract read/write set.** To detect the dependency between any two handlers, two sets, namely the read and write set, are needed. The read set is a set of tuples of fields of classes that handler may read. Similarly, the write set stores tuples about fields that could be modified. Suppose we have two handlers, h\(_1\) and h\(_2\). For a handler h\(_2\) to depend on h\(_1\), there are two conditions: 1) handler h\(_1\) must register earlier than h\(_2\) in the program expression; 2) h\(_2\)’s read set must conflict with h\(_1\)’s write set (read after write [21]), or h\(_2\)’s write set conflicts with either the read set of h\(_1\) (write after read [21]) or the write set of h\(_1\) (write after write [21]). That is because, in the sequential semantics, h\(_2\) should view the changes by h\(_1\), while h\(_2\)’s changes are invisible to h\(_1\), neither should the changes of h\(_2\) be overwritten by the changes of h\(_1\).

Notice that a subscriber o could also be a publisher for an event, say p. Then the read/write set of o could be enlarged over time, because new handlers for p may register later and the effects of these new handlers should propagate to o. Pānini does these updates automatically when new handlers register for a certain event.

**Event’s Subject list.** Subjects are formed into a list for an event. Thus, when a handler registers, its changes could be passed to these subscribers, and these subscribers merge the changes and recursively pass changes to other events when necessary. This continues until a fix point is reached (that is, no more effects are added to the publishers).

**Handlers’ Hierarchy.** Pānini groups handlers into hierarchies. In the first level, all the handlers have no dependency between any other handlers, while any handler in the second level depends on a subset of the handlers in the first level and no other handlers. Similarly handlers in the third level may depend on handlers on the first two level, but no handlers in any other level. It is possible that the effects of one handler will become larger (mentioned in the previous paragraph) and in response to this, Pānini will reorder the hierarchy dynamically.

**Event registration.** When an event handler, say h, registers, we first propagate its effects to the publishers in the publishers list, then the dependencies between h and the previous registered handlers are computed based on the read / write set. After dependencies are calculated, the handler is put into a proper level of the hierarchy.

**Event announcement and task scheduling algorithm.** When a publisher fires an event, Pānini executes the handlers in the first level concurrently. After all the handlers in this level are done, handlers in the next level are released and run in parallel until all the handlers are finished.

The computation of the dependency and the effect propagation is done when handlers register, based on the assumption that in a program, the number of announcements considerably outweighs the number of registrations. Therefore, the overhead of effects manipulation is amortized. More details can be found in Appendix A and Appendix B.

### 2.4 Properties of Pānini’s Design

In this subsection, we study the key properties of Pānini’s design. We show that our language design has the following desirable properties. Well-typed Pānini programs do not get stuck and are free of races and deadlocks. The proof of this uses a standard preservation and progress argument [47] and is presented in Appendix C.

### 2.5 Pānini’s Runtime System and Compiler

We designed an extension of Java to have asynchronous, typed events and implemented a compiler for this extension using the JastAddJ extensible compiler system [13]. As its backend, Pānini’s runtime system uses the fork/join framework [27]. This framework uses the work stealing algorithm [7] and works well for recursive work algorithms. We observed that handlers usually also act as subjects and recursively announce events, thus Pānini was built based on this framework. When an event is announced by a publisher, all handlers that are applicable are wrapped and put into the fork/join framework and may execute concurrently. Below we describe key parts of our implementation strategy.
public interface GenAvailable {
    public Generation g();
}

public interface EventHandler extends IEventHandler {
    public void ChangedHandle(Generation g);
}

public interface EventPublisher extends IEventPublisher {
    public static void register(IEventHandler handler) {
        /* 1. check whether this has register before, */
        /* wrapping the handlers using GenAvailableTask */
        // iterate all the handlers registered in the event
        for( EventHandler eh:... ) {
            /* for( EventHandler eh:... ) */
            // tasks is of type GenAvailableTask
            /* wrapping the handlers using GenAvailableTask */
            PaniniTask.coInvoke(tasks);
            /* other helper methods elided */
        }
    }
}

public static class GenAvailableTask extends PaniniTask {
    /* public run(){ execute the handler method } */
}

public static class EventFrame implements GenAvailable {
    public static void register(IEventHandler handler) {
        /* 1. check whether this has register before, */
        if yes return ( no duplicate registration )
        if no goto 2.
        /* 2. manipulate the effects of the handler */
        /* 3. insert it into the handler hierarchy */
        /* other helper methods elided */
    }
    public static void announce(GenAvailable ev) {
        /* for( EventHandler eh:... ) */
        // iterate all the handlers registered in the event
        for( EventHandler eh:... ) {
            /* for( EventHandler eh:... ) */
            // tasks is of type GenAvailableTask
            /* wrapping the handlers using GenAvailableTask */
            PaniniTask.coInvoke(tasks);
            /* other helper methods elided */
        }
    }
}

public class EventFrame {
    public void ChangedHandle(Generation g) {
        /* 1. check whether this has register before, */
        /* wrapping the handlers using GenAvailableTask */
        // iterate all the handlers registered in the event
        for( EventHandler eh:... ) {
            /* for( EventHandler eh:... ) */
            // tasks is of type GenAvailableTask
            /* wrapping the handlers using GenAvailableTask */
            PaniniTask.coInvoke(tasks);
            /* other helper methods elided */
        }
    }
}

3. Evaluation

In union there is strength. – Aesop

This section describes evaluation of design and performance benefits of Pähini’s design.

3.1 Analysis of Modularity and Concurrency Synergy

Pähini’s design can be seen as promoting both modularity and concurrency goals for a class of requirements that are typically modularized using behavioral design patterns [19]. The goal of this section is to analyze “if a program is modularized using Pähini’s features does that also expose potential concurrency in its execution?”

We have already presented one such case in Section 1, where modularization of various concerns in the implementation of a genetic algorithm exposed potential concurrency between these concerns. To further assess Pähini’s ability to achieve a synergy between modularity and concurrency goals, we have implemented several representative examples and they worked out beautifully. In the rest of this section, we present three examples. In these examples, we use Java extended with Pähini, which is supported by our compiler.

3.1.1 Concurrency in Compiler Implementations

In the art of writing compilers, performance often has higher priority than modularity. Compiler designers employ all kinds of techniques to optimize their compilers. For example, merging transformation passes which perform different transformation tasks in the same traversal, is a common practice in writing multi-pass compilers. However, the implementation of this technique usually suffers the problem of code-tangling: implementations of different concerns (i.e., transformation tasks) are all mixed together.
Figure 5 illustrates this via snippets from an abstract syntax tree (AST). It shows concerns for method declarations, expressions, and two concrete expressions: a sequence expression ($e_1; e_2$) and a field get expression ($e.f$). As an example compiler pass, we show computation of effects for these AST nodes. The effect computation concern is scattered and tangled with the AST nodes. This is a common problem in compiler design where the abstract syntax tree hierarchy imposes a modularization based on language features whereas compiler developers may also want another modularization based on passes, e.g. type checking, error reporting, code generation, etc [13]. The visitor design pattern solves this problem to a certain extent but it has other problems [13].

Figure 6. Pānini’s version of visiting an abstract syntax tree.

Pānini handles this modularization problem readily as shown in Figure 6. In this implementation, we introduce a method `visit` in each AST node. This method recursively visits the children of the node. At the same time, it announces events corresponding to the AST node. For example, a method declaration announces an event of type `MethodVisited` declared on line 1 and announced on line 8. Similarly, the AST node sequence expression and field get expression announce events of type `SequenceVisited` and `FieldGetVisited` on lines 16 and 22 respectively.

The implementation of effect computation is contained in the class `ComputeEffect`. This class has two binding declarations that say to run the method `start` when an event of type `MethodVisited` is announced and add when an event of type `FieldGetVisited` is announced. The constructor for this class registers itself to receive event announcements and initializes a hashtable to store effects per method. The method `add` inserts a read effect in this hashtable corresponding to the entry of the current method.

This Pānini program manifests a few design advantages. First AST implementation is completely separated from effect analysis. Also, unlike the visitor pattern, the `ComputeEffect` class need not implement a default functionality for all AST nodes. Furthermore, other passes such as type checking, error reporting, code generation, etc can also reuse the events declared for computing effects.

Last but not least, in Pānini, the effect computation (by the class `ComputeEffect`) could be processed in parallel with other compiler passes, like type checking. In case a compiler pass does transformation of AST nodes, Pānini’s type system will detect this as interference and automatically generate an order of their execution that would be equivalent to sequential execution. Thus, for this example Pānini shows that it can reconcile the modularity and concurrency goals such that modular design of compilers also improves their performance on multi-core processors.

3.1.2 Modular and Concurrent Image Processing

This example is adapted from and inspired by the ImageJ image processing toolkit [23]. For simplicity, assume that this library uses a class `List` and `Hashtable` similar to the classes in the `java.util` package. We have also omitted the irrelevant initializations of these classes. The class `Image` (lines 27-33) maintains a list of pixels. The method `set` for this class (lines 29–33) sets the value of a pixel at a given location to the specified integer value.

An example requirement for such a collection could be to signal changes of elements as an event. Other components may be interested in such events, e.g. for implementing incremental functionalities which rely on analyzing the increments. One such requirement for a list of pixels is to incrementally compute the Nonparametric Histogram-Base Thresholding [1, 20]. The threshold functionality may not be useful for all applications that use the image class, thus it would be sensible to keep its implementation separate from the image class to maximize reuse of the image class. Pānini’s implementation allows the threshold computation concerns to remain independent of the image concerns, while allowing their concurrent execution.

3.1.3 Overlapping Communication with Computation via Modularization of Concerns

Our next example presents a simple application for planning a trip. Planning requires finding available flights on the departure and return dates as well as a hotel and rental car for the duration of the trip. To find each of these items the program must communicate with services provided by other providers and each computation can be run independently.
event Changed{
    Image pic;
}

class Percentile extends Object{
    when Changed do compute;
    Hashtable h; /* Other fields omitted */
    Percentile()
        { register(this); h = new Hashtable(); }
    void compute(Image pic){
        /* Computationally intensive code for computing percentile based thresholds.*/
        values.put(pic, threshold);
        h.add(pic, threshold);
    }
}

class GlasbeyThreshold extends Object{
    when Changed do compute;
    Hashtable values;
    GlasbeyThreshold init(){
        register(this);
    }
    void compute(Image pic){
        /* Computationally intensive code for computing another type of threshold */
        values.put(pic, threshold);
        h.add(pic, threshold);
    }
}

class Image extends Object{
    List pixels;
    Image set(Integer i, Integer v){
        pixels.setAt(i,v);
        announce Changed(this);
        this
    }
}

Figure 7. An Image and Threshold Computation in Pānini.

In this example the context variable tripData is used to both provide the handlers with information and to give the handlers a place to store their results. As with the above example, initialization functions are elided. The FilterCars handler is also elided. The lists are filtered with an event since filtering the lists can be done independently and concurrently. In this example as well Pānini’s design shows the potential of reconciling modularity goals with concurrency goals. When an event of type PlanTrip is announced each of the three handlers can execute concurrently.

3.2 Performance Evaluation

The goal of this section is to analyze “how well do the Pānini programs perform compared to a hand-tuned concurrent implementation of equivalent functionality?”

We first describe our experimental setup and then analyze speedup realized by Pānini’s implementation as well as the overheads.

All experiments were performed on a system with a total of 12 cores (2x6-core AMD Opteron 2431 chips) running Fedora GNU/Linux.

3.2.1 Concurrency Benchmark Selection

To avoid bias in the performance measurement, we picked already implemented concurrent solutions of five computationally intensive kernels: Euler number, FFT, Fibonacci, integrate, and merge sort. Hand-tuned implementations of these kernels were already available [27].

Each program takes an input to vary the size of the workload (Euler: number of rows, FFT: size of matrix \(2^n\), Fibonacci: \(x^{th}\) Fibonacci number, integrate: number of exponentials, and merge sort: array size \(2^n\) ) For each example program, a sequential version was tested as well as concurrent versions ranging from 1 to 14 threads. Furthermore, three concurrent versions were tested:

1. an implementation using the fork/join framework [27],
2. a Pānini version with no conflict between handlers, and
3. a second Pānini’s implementation that was intentionally designed to have conflicts between handlers.

For example, calculating a Fibonacci number, \(fib(n)\), is done by recursively calculating two subproblems, \(fib(n - 1)\) and \(fib(n - 2)\). With the fork/join framework, each of these subproblems is done by a separate thread. When both of these threads are completed, the spawning thread adds them together. For Pānini, each of these subproblems is handled in separate handlers. In the case with no conflicts, these are the only two handlers. In the case with conflicts, a third handler takes the result of the two handlers for the subproblems and
3.2.2 Speedup over Sequential Implementation

We now analyze the speedup of parallel programs achieved by the Pānini’s implementation as compared to the standard fork/join model [27].

Figure 9 shows a summary comparison of speedup between the three versions. In this figure, the average speedup across all five benchmarks was taken. For each program, large input sets were used (Euler: 39, FFT: 24, Fibonacci: 55, integrate: 7, and merge sort: 25). The line in the figure represents optimal speedup.

This figure shows that the speedups between the three styles are comparable. Speedups for fork/join and Pānini without conflicts are nearly the same.

A statistical analysis shows that for all benchmarks, we do not see a statistically significant difference ($p < 0.05$) between fork/join and Pānini with no conflicts.

From the figure, we can also see that Pānini with conflicts has slightly lower speedup than both fork/join and Pānini with conflicts, however, this decrease is rather small (average 6.5% decrease from fork/join). We found a statistically sign-

ificant difference ($p < 0.05$) between fork/join and Pānini with conflicts. Note that since we are using a machine with 12 cores, performance levels off at 12 threads.

3.2.3 Overhead over the Sequential Implementation

We also measured the overhead involved with Pānini as compared to the standard fork/join model.

We first consider the average overhead across all benchmarks as shown in Figure 10. Overhead is calculated by determining the increase in runtime from the sequential version to the concurrent version with a single thread. For this figure, we use large input sizes.

This figure shows us that while Pānini increases the overhead over fork/join, it is not a prohibitive amount. For example, from fork/join to Pānini with no conflicts, we only see a 7.7% increase in overhead. Similarly, adding conflicts to Pānini only incurs an additional 6.1% overhead.

Figure 11 shows a summary comparison of overhead as program input size changes. In this figure, the overhead for the Fibonacci program is shown with a variety of input sizes. Again, overhead is calculated by determining the increase in runtime from the sequential version to the concurrent version with a single thread.

This figure shows that as input size increases, overhead decreases. In this case, overhead decreases to as low as 5.5% additional overhead for Pānini with no conflicts. Pānini with conflicts only incurs an additional 1.2% overhead for larger input sizes. Each of the differences in overhead (fork/join vs Pānini without conflicts, fork/join vs Pānini with conflicts, and Pānini with vs Pānini without conflicts) was statistically significant ($p < 0.05$) across all benchmarks.

3.3 Summary of Results

In summary, Pānini shows speedups which scale as well as the standard fork/join model. Even though Pānini has a higher overhead than fork/join, Pānini performs nearly as well as the fork/join model in terms of speedup for nearly all cases. This is all achieved without requiring explicit con-
currency and while encouraging good modular design and ensuring that programs are free of deadlocks and have sequential semantics.

4. Related Work

concuritate, mediate, harmonize, settle, accommodate, reunite – entry for "reconcile" in Roget's II

Like Jade [39], Păini is an implicit concurrency language. Programmers in Jade supply information about the effect of tasks so that the implementation may discover concurrency. Păini is different in that it relies on its type system to maximize its effects and removes the burden on the programmer to supply these effects by hand. Păini also removes any errors which could be introduced by incorrect specification of effects. This is different from Grace [6] which is an explicit threading language. Grace executes threads speculatively. If a conflict is detected, it rolls back the changes. If no races are detected it commits the changes. Păini detects conflict when handlers register.

Like X10 [8], Păini does not feature any construct for explicit locking. However, X10 is an explicit concurrency language and it uses atomic blocks for lock-free synchronization and uses the concept clocks as synchronization between activities. The Task Parallel Library (TPL) [28], wraps computation into tasks and uses thread stealing as the underlying implementation. This is similar to Păini’s runtime, but programmers in TPL have to explicitly account for races, whereas Păini’s effect system automatically avoids all races.

Events have a long history in both the software design [11, 17, 29, 31, 44] and distributed systems communities [16, 24, 32]. Păini’s notion of asynchronous, typed events build on these notions, in particular recent work in programming languages focusing on event-driven design [14, 15, 36]. In software design, events and implicit-invocation have been seen as a decoupling mechanism for modules [31, 44], whereas in distributed systems, events are seen as a mechanism of decoupling component execution for location transparent deployment and extensibility [32]. New to Păini’s design and its philosophy is the unification of design and execution decoupling.

Păini’s design is also not the first to promote implicit concurrency. For example, in POOL [3], ABCL [48], Concurrent Smalltalk [49] and BETA [43], objects implicitly execute in the context of a local process. This is different from Păini where only handler instances are run implicitly and concurrently. This allows smoother integration with mainstream programming languages such as Java. This also permits an easier integration of our event-based model with the thread-based explicit concurrency models as promoted by Li and Zdancewic [29]. In this work, we do not discuss the semantic issues with this integration, however.

Other recent work such as TaskJava [17] in the programming language design setting and Tame [24] and Tasks [11], in the distributed systems setting, have promoted similar integration with existing languages. Fischer et al. in their work on TaskJava [17] introduce task, similar to a handler in Păini. An asynchronous method is marked with the keyword async, indicating that it could block and should be compiled into continuation-passing style. This method may use a primitive wait to express its interests in a set of events and this expression will block until one of them fires. Krohn et al. have promoted similar ideas in Tame [24] where they use a primitive twait to block on events. In both these approaches, running of the concurrent task is explicitly managed by the programmer. In Păini, however, handlers are implicitly spawned and managed by the language runtime. As a result, programmers are relieved of reasoning about locking and data race problems. Such software engineering properties are becoming very important with the increasing presence of concurrent software, increasing interleaving of threads in concurrent software, and increasing number of under-prepared software developers writing code using concurrency unsafe features.
Unlike Multilisp [40], which has the future construct, Päñini uses different expressions as synchronization points. Moreover, unlike Java’s current adoption of Futures, which is unsafe [46], heap access expressions in Päñini are sound. Furthermore, unlike previous work [35, 46], our implementation doesn’t require modifications to the virtual machine.

5. Conclusion and Future Work

Faith is taking the first step even when you don’t see the whole staircase. – Martin Luther King Jr.

Language features that promote concurrency in program design have become important [2, 5]. Explicit concurrency features such as threads are hard to reason about and building correct software systems in their presence is difficult [33, 42]. There have been several proposals for concurrent language features, but none unifies program design for modularity with program design for concurrency. In the design of Päñini, we pursue this goal. In an effort to do so, we have developed the notion of events that are especially helpful for programs where modules are decoupled using implicit-invocation design style [12, 31, 32, 44]. Event announcements provide implicit concurrency in program designs when events are signaled and consumed. We have tried out several examples, where Päñini improves both program design and potential available concurrency.

For future work, we would like to apply Päñini’s design to large projects and to evaluate whether the reconciliation of modularity and concurrency goals that we saw in our examples is scalable to larger software projects.

Acknowledgments

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References

[22] A. Igarashi, B. Pierce, and P. Wedler. Featherweight Java: A minimal core calculus for Java and GJ. In OOPSLA, pages 132–146, 1999.
[24] M. Krohn, E. Kohler, and M. F. Kaashoek. Events can make


[33] J. Ousterhout. Why threads are a bad idea (for most purposes). In ATEC, January 1996.


A. Pānini’s Type and Effect System

Type checking uses the attributes defined in Figure 12. Compared to Ptolemy [36] new to Pānini is its effect system. For example, the type attributes for expressions are represented as \((t, \rho)\), the type of the expression \((t)\) and its effect set \((\rho)\).

\[ \theta ::= \text{OK} \mid \{ (t_1 \times \ldots \times t_n \rightarrow t, \rho) \} \in \text{c} \mid \{ (t, \rho) \} \]

\[ \rho ::= \epsilon + \rho \mid * \]

\[ e ::= \text{read} \; c \; f \]

\[ \text{write} \; c \; f \]

\[ \text{ann} \; p \]

\[ \text{reg} \; c \]

\[ \text{create} \; c \]

\[ \pi, \Pi ::= \{ \{ t : \theta \}_i \}_{i \in K}, \quad \text{where } K \text{ is finite, } K \subseteq (\mathcal{C} \cup \{ \text{this} \} \cup \mathcal{V}) \]

\[ \sigma \]

\[ \text{prog} \leftrightarrow \text{toplevel decl/ body types} \]

\[ \text{method types} \]

\[ \text{expression types} \]

\[ \text{program effects} \]

\[ \text{read effect} \]

\[ \text{write effect} \]

\[ \text{announce effect} \]

\[ \text{register effect} \]

\[ \text{new object effect} \]

\[ \pi, \Pi \]

\[ \text{type environments} \]

Figure 12. Type and Effect Attributes.

The effects are used to compute the potential conflicts between handlers. These effects include: 1) read effect: a class and a field to be read; 2) write effect, content is similar to read effect; 3) announce effect: what event a certain expression may announce; 4) register effect and 5) new object. The interference between the effects is shown in Figure 13.

<table>
<thead>
<tr>
<th>Effects</th>
<th>read</th>
<th>write</th>
<th>ann</th>
<th>reg</th>
<th>create</th>
</tr>
</thead>
<tbody>
<tr>
<td>read</td>
<td>✓</td>
<td>✓</td>
<td></td>
<td></td>
<td>✓</td>
</tr>
<tr>
<td>write</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td></td>
<td>✓</td>
</tr>
<tr>
<td>ann</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>reg</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>create</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
</tbody>
</table>

Figure 13. Effect Interference. ✓: conflicts, ×: no conflicts

Read effects will not interfere with each other. Write effects will conflict if either another read or write effect accesses the same field of the same object. Announce effects will interfere with register effects, because the order of registration affects the set of handlers run during announcement. Announce effect is also used later in the semantics because handlers could also act as publishers (refer to as handler/publisher) and announce events (e). Pānini updates the effects of these handler/publisher(s) every time a handler registers the event e. Thus Pānini will get more accurate information about the effects of the handlers/publishers when scheduling and reduce false interferences. In Pānini, register effects will interfere with read/write effects as well, due to the fact that after a register, a handler could introduce unforeseen read and write effects and thus complicate the interference.

New object effect will not interfere with any other effect. The new object effect is used to reduce false interference. Certain variables are marked as create if type checking detects that these variables point to newly created objects. We observe that new objects are not the sources for interference for the following reasons:

1. if a newly created object does not escape from the handler the object cannot be accessed by any other handlers, thus there will not be any race;

2. otherwise, assume that a newly created object \((o_n)\) escapes the handler \((h_1)\) and is referenced by another handler \((h_2)\), then the program will first have to change a field of another object (referred to as \(o_m\)) to point to \(o_n\) to make it escape. On the other hand, \(h_2\) will have to read the field of \(o_m\), which will be detected by Pānini and reported as an interference (because \(h_1\) changes the field of \(o_m\) and \(h_2\) reads the field of \(o_n\)). That is to say, it will not be the newly created object that causes data races.

Thanks to this observation, Pānini could safely remove the read/write effect of any newly created object and thus reduce false interferences to a considerable extent.

The type checking rules are shown in Figures 14 and 16. The notation \(\nu' < \nu\) means \(\nu'\) is a subtype of \(\nu\). It is the reflexive-transitive closure of the declared subclass relationships [36]. We state the type checking rules using a fixed class table (list of declarations \(CT\)) as in Clifton’s work [9,10]. The class table can be thought of as an implicit inherited attribute used by the rules and auxiliary functions. We require that top-level names in the program are distinct and that the inheritance relation on classes is acyclic. The typing rules for expressions use a simple type environment, \(\Pi\), which is a finite partial mapping from locations \(loc\) or variable names \(var\) to a type.

(T-PROGRAM)

\[
\forall \text{decl} \in \text{decl} \implies \vdash \text{decl} : \text{OK}
\]

\[
\vdash \text{decl} : \{ (t, \rho) \}
\]

(T-EVENT)

\[
\forall (t, \text{var}) \in t \text{var} \implies \text{isType}(t, \rho)
\]

\[
\vdash \text{event} : \{ (t, \rho) \}
\]

(T-CLASS)

\[
\text{validF}(\mathcal{F}, d)
\]

\[
\vdash \text{class} : \text{extends} \{ \{ t, \text{meth}binding \} : \text{OK} \}
\]

(T-METHOD)

\[
\text{isClass}(t)
\]

\[
\vdash \forall \text{meth} \in \text{meth} : \text{isType}(\{ t, \rho \}_j)\text{ in C}
\]

\[
\text{var} : \{ t_1, \ldots, t_n \}_j : \text{this} \implies \epsilon : \{ t, \rho \}
\]

\[
\epsilon \in \text{overrides}(m, c, \{ t_1 \times \ldots \times t_n \rightarrow t, \rho \})
\]

\[
\vdash \epsilon \in \text{method}binding(m, c) \text{ in c}
\]

(T-BINDING)

\[
\text{CT}(p) = \epsilon \in \text{event} \{ t, \text{var'} \}
\]

\[
\epsilon \in \text{method}(c, m) \implies \text{findMethod}(c, m)
\]

\[
\pi = \{ t_1, \ldots, t_m \}
\]

\[
\forall \text{var} : \{ t_1, \ldots, t_m \}_j
\]

\[
\vdash \text{when} \epsilon \text{do} \; m : \text{OK} \in c
\]

Figure 14. Type and Effect Rules for declarations [9,10,36].

The (T-PROGRAM) rule says that the entire program type checks if all the declarations type check and the expression \(e\) has any type \(t\) and any effect \(\rho\).

The (T-EVENT) rule says that an event declaration type checks, if all the types of all the fields are declared properly. The auxiliary function isType (shown in Figure 17), looks at the class table to check if a type has been defined or not.
Valid method overriding:

\[ CT(c) = \text{\textbf{class}}\ e \text{\textbf{extends}}\ d\ \text{\{field1 method1...methen bind1...bindp\}} \]
\[ \forall i \in \{1...p\} \cdot \text{methi} = t\text{\{var1, ..., varn\}}(e) \]
\[ \text{override}(m, d, (t_1 \times \ldots \times t_n \rightarrow t, \rho)) \]

\[ \text{methodType}(d, m) = (t_1 \times \ldots \times t_n \rightarrow t', \rho') \quad \rho \subseteq \rho' \]
\[ \text{override}(m, d, (t_1 \times \ldots \times t_n \rightarrow t, \rho)) \]
\[ \text{override}(m, \text{Object}, t_1 \times \ldots \times t_n \rightarrow t, \rho) \]

**Figure 15.** Auxiliary functions used in type rules [9, 10].

<table>
<thead>
<tr>
<th>Rule</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>(T-NEW)</td>
<td>( \text{inClass}(c) )</td>
</tr>
<tr>
<td>(T-CAST)</td>
<td>( \text{isType}(t) ) ( \vdash e : (u, \rho) ) ( \vdash \text{cast} e : (t, \rho) )</td>
</tr>
<tr>
<td>(T-GET)</td>
<td>( \vdash e : (c, \rho) ) ( \rho \neq {\text{create} c} ) ( \text{fieldsOf}(c)(f) = t ) ( \vdash e, f : (t, \rho) \cup {\text{read} e f} )</td>
</tr>
<tr>
<td>(T-SEQUENCE)</td>
<td>( \vdash e_1 : (t_1, \rho) ) ( \vdash e_2 : (t_2, \rho') ) ( \vdash e_1 ; e_2 : (t_2, \rho \cup \rho') )</td>
</tr>
<tr>
<td>(T-VAR)</td>
<td>( \text{var}(t) = (t, \rho) ) ( \vdash \text{isType}(t) ) ( \vdash \text{var} : (t, \rho) \vdash e : (t_2, \rho') ) ( t_1 &lt; t )</td>
</tr>
<tr>
<td>(T-NULL)</td>
<td>( \vdash \text{null} : (c, \emptyset) ) ( \vdash \text{register} : (c, \emptyset) \cup {\text{reg}} )</td>
</tr>
<tr>
<td>(T-SET)</td>
<td>( \vdash e : (c, \rho) \neq {\text{create} c} ) ( \text{fieldsOf}(c)(f) = t ) ( \vdash e, f : (t, \rho \cup {\text{write} c f}) )</td>
</tr>
<tr>
<td>(T-SET-LOCAL)</td>
<td>( \vdash e : (c, {\text{create} c}) ) ( \text{fieldsOf}(c)(f) = t ) ( \vdash e, f : (t', \rho') ) ( t' &lt; t )</td>
</tr>
<tr>
<td>(T-ANNOUNCE)</td>
<td>( CT(p) = \text{\textbf{event}}\ p\ {t_1 \text{var1} \ldots \text{t_n varn}} ) ( \forall i \in {1...n} \cdot \vdash \text{e}_i : (u_i, \rho) \cup u_i &lt; t_i ) ( \vdash e : (t, \rho) ) ( \vdash \text{announce} p : (\text{e}_1, \ldots, \text{e}<em>n) e : (t, \text{ann} p) \cup \bigcup</em>{i=1}^{n} p_i \cup u_i )</td>
</tr>
<tr>
<td>(T-CALL)</td>
<td>( e_0 : (c_0, \rho_0) ) ( \forall i \in {1...n} \cdot \vdash \text{e}<em>i : (u_i, \rho_i) \cup u_i &lt; t_i ) ( \vdash e_0, m(e_1, \ldots, e_n) : (t, \rho \cup \bigcup</em>{i=1}^{n} p_i \cup u_i) )</td>
</tr>
<tr>
<td>(T-YIELD)</td>
<td>( \vdash e : (t, \rho) ) ( \vdash \text{yield} e : (t, \rho) )</td>
</tr>
</tbody>
</table>

**Figure 16.** Type and Effect rules for expressions [9, 10, 36].

The (T-CLASS) rule says that a class declaration type checks if all the following constraints are satisfied. First, all the newly declared fields are not fields of its super class (this is checked by the omitted auxiliary function \( \text{validF} \)). Next, its super class \( d \) is defined in the Class Table. Finally, all the declared methods and bindings type check.

The (T-METHOD) rule says that a method declaration type checks if all the following constrains are satisfied. First, the return type is a class type. Next, if all the parameters have their corresponding declared types, the body of the method has type \( u \) and effect \( \rho \). Also \( u \) is a subtype of \( t \). This rule also uses an auxiliary function \( \text{override} \), defined in Figure 15. In addition to standard conditions, this function enforces that the effect of an overriding method is the subset of the effect of overridden method\(^2\).

The (T-BINDING) rule says that a binding declaration type checks, if the named method is properly defined; all the context variables are subtypes of their corresponding declared types in the method; and the named event type is declared.

The type rules for the expressions are shown in Figure 16. Most rules for typing expressions are straightforward.

The (T-NEW) rule says that a new expression has the type of the class being declared if the class \( c \) has been properly declared and has a single effect \( \text{create} \) to denote that this is a newly created object as mentioned previously to reduce false interferences.

The (T-CAST) rule says that for a cast expression, the cast type must be a class type, and its effect is the same as the expression’s.

The (T-GET) rule says that a field access expression returns the type of the field of the class, the effects of it will be the effect of the object expression plus a read effect.

The (T-GET-LOCAL) rule is similar to the previous rule, except that \( \text{Pāṇini} \) knows that the object expression is pointing to a newly created object and thus the read effect is redundant and deleted.

The (T-SEQUENCE) rule states that the sequence expression has same type as the last expression and its effects are the union of the two expressions.

The (T-YIELD) rule says that a \( \text{yield} \) expression has the same type and same effect as the expression \( e \).

The (T-VAR) rule checks that \( \text{var} \) is in the environment.

The (T-DEFINITE) rule for declaration expressions is similar to the sequence expression except that the initial expression should be a subtype of the type of the new variable. Also, the type of the variable is placed in the environment. Finally, the sequence expression type checks properly.

The (T-NULL) rule says that the null expression will type check and has no effect.

The (T-REGISTER) rule says that a register expression has the same type as the object expression and the effects will be the effects of the object expression plus one register effect.

The (T-SET) rule says that a field assignment expression type checks if the object expression is of a class type and the type of the assignment expression \( e_2 \) is a subtype of the type of the field of the class. The effects will be the union of

\(^2\) In practice, we enlarge the effect set of the method in the super class such that the effect of the overriding method is a subset of its super class.
the effects of its two subexpressions plus one write effect since this expression is to modify a field of an object.

The (T-SET-LOCAL) rule is similar to (T-SET) except that the type system can detect that it is changing a field of a new object thus the single write is not needed.

The (T-ANNOUNCE) rule says that an announcement expression type checks if the event was declared and the actual parameters are a subtype of the declared fields type in the event declaration. The entire expression has the type of the subsequent expression $e$. The effects of the announce expression will be the union of all the parameters’ effects and the subsequent expression plus an announcement effect.

The (T-CALL) is similar to the announce expression. This rule says that for a method call expression it finds the method in the $CT$ using the auxiliary function findMeth (not shown here) and this method is declared either in its own class or its super class. Each actual argument expression is of subtype of corresponding parameter type. This method call expression has the same type as the return type of the method.

$$\text{isType}(t) = (t \in \text{dom}(CT) \land CT(t) = \text{class } t \ldots)$$

$$\text{fieldsOf}(e) = \{t \cup \text{fieldsOf}(e')\}$$

where $CT(t) = \text{class } e \text{ extends } e'(t_1 f_1; \ldots t_n f_n; \ldots)$

$$\text{validF}(\tau, e) = \forall i \in \{1..n\} : \text{isType}(t_i) \land f_i \notin \text{dom}(\text{fieldsOf}(e))$$

**Figure 17.** Auxiliary functions used in type rules [?].

### B. Pāṇini’s Operational Semantics

Here we give a small-step operational semantics for Pāṇini.

**Added Syntax:**

$$e ::= \text{loc} | \text{yield } e | \text{NullPointerException} | \text{ClassCastException}$$

where $\text{loc} \in \mathcal{L}$, a set of locations

**Evaluation relation:** $\leftrightarrow : \Sigma \rightarrow \Sigma$

**Domains:**

$$\Sigma ::= \langle \psi, \mu, \gamma \rangle$$

“Program Configurations”

$$\psi ::= \{e, \tau \} + \psi$$

“Task Configurations”

$$\tau ::= \{n, \{v_k\}_{k \in K}\}$$

“Task Local Data”

where $n_a \in \mathcal{N}$ and $K$ is finite

$$\mu ::= \{\text{loc} \rightarrow \alpha\} + \mu$$

“Stores”

$$v ::= \text{null} | \text{loc}$$

“Values”

$$o ::= [e, F]$$

“Object Records”

$$F ::= \{f_k \leftarrow v_k\}_{k \in K}, \text{ where } K \text{ is finite}$$

“Field Maps”

$$\gamma ::= \text{loc} + \gamma$$

“Subscriber List”

**Evaluation contexts:**

$$E ::= - | E.m(e\ldots) | v.m(v\ldots E e\ldots) | \text{cast } t E | E.f | E.f e | E.f v = E | t \text{ var } E; e | E; e | E$$

| announce(v\ldots E e\ldots); e | E.register() |

**Figure 18.** Added syntax, domains, and evaluation contexts used in the semantics, based on [9, 36].

### Intermediate Expressions.

The expression semantics rely on four expressions that are not part of Pāṇini’s surface syntax as shown in Figure 18. The loc expression represents locations in the store. Following Abadi and Plotkin [2], we use the yield expression to model concurrency. The yield expression allows other tasks to run. The rules and auxiliary functions all make implicit use of a (global) list: $CT$, the program’s declarations. The NullPointerException and ClassCastException are two final states reached: 1) when trying to access a field or a method from a null pointer object or 2) an object that is not of subtype of the casting type.

**Domains.** The small steps taken in the semantics are defined as transitions from one configuration to another. These configurations are shown in Figure 18.

A configuration consists of a task queue $\psi$, a global store $\mu$, and a global subscriber list $\gamma$. The store $\mu$ is a mapping from locations (loc) to objects ($o$). The subscriber list $\gamma$ consists of a set of receiver objects for handler methods.

The task queue $\psi$ consists of an ordered list of task configurations $\langle e, \tau \rangle$. This configuration consists of an expression $e$ running in that task and the corresponding task local data ($\tau$). The task local data is used to record the identity of the current task ($n$) and a set of identities for other tasks on which this task depends on. A task $t$ depends on another task $t'$ if $t$’s read/write set conflicts with the read/write set of $t'$. Pāṇini will never schedule a task to run unless all the tasks in its dependence set (represented as the id set) are finished.

An object record $o$ consists of a class name $c$ and a field record $F$. A field record is a mapping from field names $f$ to values $v$. A value $v$ may either be null or a reference loc, which have standard meanings.

**Evaluation Contexts.** We present the semantics as a set of evaluation contexts $E$ (Figure 18) and a one-step reduction relation that acts on the position in the overall expression identified by the evaluation context [47]. This avoids the need for writing out standard recursive rules and clearly presents the order of evaluation. The language uses the call-by-value evaluation strategy. The initial configuration of a program with a main expression $e$ is $\langle \langle e, (0, \emptyset) \rangle, \bullet, \bullet \rangle$.

**Semantics for Object-oriented Expressions.** The rules for OO expressions are given in Figure 19. These are mostly standard and adopted from the work of Rajan and Leavens [36], and Clifton’s work [9, 10].

One difference stems from the concurrency and store models in Pāṇini. The use of the intermediate expression yield in the (CALL), (SEQUENCE), and (DEFINE) rules serves to allow other tasks to run.

The (NEW) rule creates a new object and initializes its fields to null. It then creates a record with a mapping from a reference to this newly created object.

The (CALL) rule acquires the method signature using the auxiliary function findMeth (not shown here). It uses dy-
nomic dispatch, which starts from the dynamic class (c) of the
record, and may look up the super class of the object
if needed. The method body is to be evaluated with the
arguments replaced by the actual values as well as the
this variable by loc. It then yields control by calling (Yield)
to model concurrency, which will be discussed later.

The (Sequence) rule says that the current task may
yield control after the evaluation of the first expression.
The (Cast) rule is used only when the loc is a valid record
in the store and when the type of object record pointed to by
loc is subtype of the cast type. The (Define) rule allows for
local definitions. It binds the variable given to the value
and evaluates the subsequent expressions with the new binding.

The (Get) or get field read rule gets an object record
from the store and retrieves the corresponding field value
as the result. The semantics for (Set) use as an overriding
operator for finite functions. That is, if \( \mu' = \mu + (\text{loc} \mapsto v) \),
then \( \mu'(\text{loc}') = v \) if \( \text{loc}' = \text{loc} \) and otherwise \( \mu'(\text{loc}') = \mu(\text{loc}') \). The operation first fetches the object from the store
and overides the field.

Semantics for Yielding Control. In Pānini’s semantics,
like Abadi and Plotkin [2], the running task may implicitly
relinquish control to other tasks. The rules for yielding
control are given in Figure 20.

The (Yield) rule says to put the current task configuration
in the end of the task-queue and to start evaluating the next
active task configuration from the current task queue. Finding
an active task is done by the auxiliary function active
(not shown). It searches the items (task configuration) from
the head of the queue until it finds the first item that could be
run and returns it. A task configuration is ready to run if the
tasks in its dependence set are done.

The (Yield-Done) rule is applied when there are no other
task configurations in the queue. Since there are no other
tasks in the queue, it continues to evaluate the current task
configuration. The (Yield-End) rule says that the current
running task is done (it evaluates to a single value \( v \)), thus
this task configuration is removed from the queue and next
active task will be scheduled.

Semantics for Event registration. We now describe the
semantics for subscribing to an event (Figure 21).

The (Multi-Register) rule is applied when the handler
has already registered previously and thus the configuration
does not change. The (Register) rule finds out that this
handler is not the queue, so Pānini safely puts this record
at the front of the queue.

Semantics for announcing an event. The semantics for
signaling events is shown in Figure 21.

The (Announce) rule takes the relevant event declaration
from the program’s list of declarations and creates a list of
actual parameters (\( \nu \)). This list of actual parameters (\( \nu \))
is used by the auxiliary function spawn shown in Figure 22
Figure 22 (with other helper functions in Figure 23 and 24).

The function spawn searches the program’s global list of
subscribers (\( \gamma \)) for applicable handlers (using auxiliary
functions hfind, hmatch, and match). Auxiliary functions
buildconf (Figure 23) and buildconf create task configurations
for handlers. buildconf binds the context variables (of
the event type) with the values (\( \nu \)), computes an unique id

Figure 20. Semantics of yielding control in Pānini

Figure 21. Semantics of Registration and Announcement

Figure 19. Semantics of object-oriented expressions in
Pānini, based in part on [9, 10, 36]
spawn(p, ψ, γ, ν, µ) = buildconfs(H, ψ, ν, •, γ, µ)  
where H = hhfind(γ, p, µ) and CT is the program’s list of declarations

hhfind(•, p, µ) = •

hhfind(loc + γ, p, µ) = hhfind(γ, p, µ)

where µ(loc) = [c, F] and hmatch(c, p, CT) = •

hhfind(loc + γ, p, µ) = concat(hhfind(γ, p, µ), (loc, m))

where µ(loc) = [c, F] and hmatch(c, p, CT) = m

and CT is the program’s list of declarations

hmatch(c, p) = •

hmatch(c, p, (event p (• . . .)) + CT′) = hmatch(c, p, CT′)

hmatch(c, p, (class c . . .) + CT) = hmatch(c, p, CT) where c ≠ c′

hmatch(c, p, (class extends d . . . binding1, . . . bindingn) + CT′) = 

= exc-match(binding1 + . . . + bindingn, p), hmatch(d, p, CT)

where exc-match(•, H) = H and exc-match(e, H) = e

match(•, p) = •

match((when p’ do m) + B, p) = match(H, p) where p’ ≠ p

match((when p’ do m) + B, p) = m

car(•) = 0
car((loc, m) + H) = 1 + car(H)

max(•, id) = id

max((e’, (id, I)), ψ, id) = max(ψ, id) where id’ < id

max((e’, (id, I)), ψ, id) = max(ψ, id’) where id’ > id

getE(•, γ, µ) = •

getE((loc, m) + H, γ, µ) = concat(update(p, γ, µ), getE(H, γ, µ))

where loc = [c, F] and (e’, t, m, . . . , (p, η)) in e’ = findMeth(c, m)

diff(•, p) = true

diff(e’ + p, p) = diff(e’ + p) where true = differ(e, p)

diff(e’ + p, p) = false where false = differ(e, p)

differ(e, •) = true

differ(e, e’ + p) = differ(e, p) where e and e’ have no conflict

differ(e, e’ + p) = false where e and e’ have conflicts

Figure 22. Functions for Creating Task Configurations.

buildconfs(•, ψ, ν, H′, γ, µ) = (•, •)

buildconfs(loc, m) + H, ψ, ν, H′, γ, µ =

= ((e, (m, id, I)) + ψ, concat(m, id, I))

where ((e, (m, id, I)) = buildconfs(loc, m, ψ, ν, H′, γ, µ)

and H′ = H + (loc, m, ψ, ν, H′, γ, µ)

and (ψ, I′) = buildconfs(H, ψ, ν, H′, γ, µ)

buildconf(loc, m, ψ, ν, H, γ, µ) =

let e’ = (class loc extends (var1, . . . , varn) where n in e’ (id, I))

where loc = [c, F] and

(e’, t, m, t1 var1, . . . , tn varn)(e) = findMeth(c, m)

and ν = ν + . . . + vn and I = pre(loc, m, H, id + 1, γ, µ)

and id = 1 + car(H) + id and id’ = max(ψ, −1)

pre(loc, m, •, n, γ, µ) = •

pre(loc, m, (loc1, m1) + H, n, γ, µ) = pre(loc, m, H, n + 1, γ, µ)

where loc = [c, F] and (e’, t, m, ρ, p) = findMeth(e, m)

and loc1 = [c, F] and (e1’ t1, m1, . . . , ρ1) = findMeth(e1, m1)

and true = differ(update(ρ, γ, µ), update(ρ’, γ, µ))

pre(loc, m, (loc1, m1) + H, n) = concat(n, pre(loc, m, H, n + 1))

where loc = [c, F] and (e’, t, m, ρ, p) = findMeth(e, m)

and loc1 = [c, F] and (e1’ t1, m1, . . . , ρ1) = findMeth(e1, m1)

and false = differ(update(ρ, γ, µ), update(ρ’, γ, µ))

update(•, γ, µ) = •

update(read c f) + p, γ, µ = concat(update(read c f), update(p, γ, µ))

update(write c f) + p, γ, µ = concat(update(write c f), update(p, ρ, γ, µ))

update(create) + p, γ, µ = concat(update(create), update(p, γ, µ))

update(alloc) + p, γ, µ = (loc, m) + H = car(H)

getE(hhfind(γ, p, µ), γ, µ) = update(p, γ, µ)

Figure 23. Functions for building handler configurations.

effect sets with the effect set of this task. Pānini does this to get more accurate information about the potential effect sets of a task to reduce false conflicts. The functions diff and differ are used to actually compared the effects. The table in Figure 13 is used to compare effects. A read effect will conflict with a write effect if they access the same field of the same class or a subclass of another class. A read effect also conflicts with the register effect. A write effect will conflict with another write effect similar to the read effect discussed above. An announce effect conflicts with only register effects and register effects will conflict with any effect except for the create effect.

C. Properties of Pānini’s Design

Definition C.1. [Blocked Configurations.] An expression e and its task local τ = ⟨n, {nk}⟩ in a configuration ⟨⟨e, τ⟩, ψ, µ, γ⟩ may block if any one of its predecessors is still in execution.

Theorem C.2. [Liveness.] Let ⟨⟨e, τ⟩, ψ, µ, γ⟩ be an arbitrary program configuration, where e is a well-typed expression, τ is a task local, µ is the store, ψ is a task queue and γ is a handler queue. Then either e is not blocked or there is some task configuration in ψ that is not blocked.

Proof Sketch: We could construct a tree using the tasks, where a parent node publishes an event and the handlers form the children of the tree. So, in this case, nodes in the lower level will never depend on nodes in the level above. A node may depend on its children when it is publishing an event or it could depend on a sibling if its effect set conflicts with the sibling’s. Thus, in the lowest level of the three (leaves), there is at least one task (the handlers in this level register earlier than any other handlers in the tree) that does not block.

Therefore, a well type Pānini program does not deadlock.
\section{Proof of Type Soundness}

A standard preservation and progress argument \cite{47} is used to prove the soundness of Païnini's type system. The details are adapted from previous work \cite{9,10,18}. Throughout this section we assume a fixed, well-typed program with a fixed class table, CT. A type environment \( \Pi := \{ I : \{ t, \rho \} \} \) maps variables and store locations to types and effect sets. The effect set was used in the semantics to compute the dependency between handlers and will not be used in the following section. For simplicity, we omit \( \rho \) in subsequent discussion. The key definition of consistency is as follows.

\begin{definition}[Environment-Store Consistency]
Suppose we have a type environment \( \Pi \) and \( \mu \) a store. Then \( \Pi \) is consistent with \( \mu \), written \( \Pi \approx \mu \), if and only if all the followings hold:
\begin{enumerate}
\item \( \forall \text{loc} \cdot \mu(\text{loc}) = [t,F] \Rightarrow \\
(\ \alpha) \Pi(\text{loc}) = t \qquad \text{and} \\
(\ \beta) \text{dom}(F) = \text{dom}(\text{fieldsOf}(t)) \quad \text{and} \\
(\ \gamma) \text{rng}(F) \subseteq \text{dom}(\mu) \cup \{ \text{null} \} \quad \text{and} \\
(\ \delta) \forall f \in \text{dom}(F) \cdot F(f) = \text{loc}', \text{fieldOf}(t)(f) = u \\
\text{and } \mu(\text{loc}') = [t',F'] \Rightarrow t' <: u \\
\end{enumerate}
\end{definition}

\begin{lemma}[Substitution]
If \( \Pi, \var_1 : t_1, \ldots, \var_n : t_n \vdash e : t \) and \( \forall i \in \{ 1, \ldots, n \} \cdot \Pi \vdash e_i : s_i \) where \( s_i <: t_i \) then \( \Pi \vdash [\var_1/e_1, \ldots, \var_n/e_n]e : s \) for some \( s <: t \).
\end{lemma}

\begin{proof}
Sketch: Let \( \Pi' = \Pi, \var_1 : t_1, \ldots, \var_n : t_n \) and \( [\var'/e'] = [\var_1/e_1, \ldots, \var_n/e_n] \). The proof proceeds by structural induction on the derivation of \( \Pi \vdash e : t \) and by cases based on the last step in that derivation. The base cases are (T-NEW), (T-NULL) and (T-VAR), which have no variables and \( s = t \). Other cases can be proved by adaptations of MiniMAO_0 \cite{9}. The induction hypothesis (IH) is that the lemma holds for all sub-derivations of the derivation. The cases for (T-CAST), (T-SEQUENCE), (T-SET), (T-SET-LOCAL), (T-CALL), (T-GET) and (T-GET-LOCAL) are similar to Clifton’s proofs. We now consider the case for (T-REGISTER), (T-ANNOUNCE) and (T-YIELD).

For \( \text{announce} \ p (e_1, \ldots, e_n) ; e_{n+1} \), we do the same substitution for each argument \( e_i, 1 \leq i \leq n \). By IH, each of these has a subtype of the argument. Also, by IH, the substitution of \( e_{n+1} \), \( [\var'/e']e_{n+1} \) has the subtype of \( e_{n+1} \). Therefore, since the whole expression has the same type as \( e_{n+1} \), consistency holds.

The cases for \( \text{yield} e \) and \( \text{register}(e) \) are straight-forward, because the type of \( \text{yield} e \) and \( \text{register}(e) \) is the same as \( e \).
\end{proof}

\begin{lemma}[Environment Extension]
If \( \Pi \vdash e : t \) and \( a \notin \text{dom}(\Pi) \), then \( \Pi, a : t' \vdash e : t \).
\end{lemma}

\begin{lemma}[Environment Contraction]
If \( \Pi, a : t' \vdash e : t \) and \( a \) is not free in \( e \), then \( \Pi \vdash e : t \).
\end{lemma}

\begin{lemma}[Replacement]
If \( \Pi \vdash \text{E}[e] : t, \Pi \vdash e : t' \), and \( \Pi \vdash e' : t', \) then \( \Pi \vdash \text{E}[e'] : t' \).
\end{lemma}

\begin{lemma}[Replacement with Subtyping]
If \( \Pi \vdash \text{E}[e] : t, \Pi \vdash e : u \), and \( \Pi \vdash e' : u' \) where \( u' <: u \), then \( \Pi \vdash \text{E}[e'] : t' \) where \( t' <: t \).
\end{lemma}

\begin{theorem}[Progress]
For a well-typed expression \( e \), a task local \( t \), a task queue \( \psi \), a store \( \mu \) and a handler queue \( \gamma \). If \( \Pi \vdash e : t \) and \( \Pi \approx \mu \), then either
\begin{itemize}
\item \( e = \text{loc} \) or
\item \( e = \text{null} \) or \( e = \text{NullPointerException} \) or \( e = \text{ClassCastException} \) or \( e = \text{null}.f.\text{null}.f = v, \text{null}.m(v_1, \ldots, v_n), \text{register}(\text{null}) \) and \( \text{cast } e \) (we omitted these rules where standard exceptions happen). These values serve as the base cases.
\item In the case where the expression \( e \) is not a value, evaluation rules are considered case by case for the proof. We proceed with the induction of derivation of expression \( e \). Induction hypothesis (IH) assumes that all sub-terms of \( e \) progress and are well-typed.
\end{itemize}
\end{theorem}

\begin{proof}
Sketch:
\begin{itemize}
\item (a) If \( e = v \) or \( e = \text{null} \), it is trivial.
\item (b) Cases \( e = \text{null}.f.\text{null}.f = v, \text{null}.m(v_1, \ldots, v_n), \text{register}(\text{null}) \) and \( \text{cast } e \) (we omitted these rules where standard exceptions happen). These values serve as the base cases.
\item (c) In the case where the expression \( e \) is not a value, evaluation rules are considered case by case for the proof. We proceed with the induction of derivation of expression \( e \). Induction hypothesis (IH) assumes that all sub-terms of \( e \) progress and are well-typed.
\end{itemize}
\end{proof}

\begin{theorem}[Subject-reduction]
Let \( e \) be an expression and \( e \neq \text{yield} e_1 \) for any \( e_1, \tau \) task local, \( \psi \) a task queue, \( \mu \) a store, and \( \gamma \) a handler queue. Let \( \Pi \approx \mu \) be a type environment and \( t \) a type. If \( \Pi \vdash e : t \) and \( \langle e, \tau \rangle + \psi, \mu, \gamma \rangle \rightarrow \langle e', \tau' \rangle + \psi', \mu', \gamma' \rangle \), then there is some \( \Pi' \approx \mu' \) and \( t' \) such that \( \Pi' \vdash e' : t' \) and \( t' <: t \).
\end{theorem}

\begin{proof}
Proof Sketch: The proof is by cases on the definition of \( \rightarrow \) separately. The cases for object oriented parts (rules (NEW), (NULL), (CAST), (GET), (SET), (VAR) and (CALL)) can be proved by adaptations of Clifton’s proofs for MiniMAO_0 \cite{9, Section 3.1.4}.
\end{proof}
The rule for (Sequence) is similar to Clifton’s work, except that \( e' = E[yield \ e] \) instead of \( e' = E[e] \). Since the type of \( yield \ e \) has the same type as \( e \), this case holds.

For (Define), \( e = E[t \ var = v; e_1] \) and \( e' = E[\var[v]e_1] \):

\[
\tau' = \tau, \mu' = \mu, \psi' = \psi, \gamma' = \gamma \text{ and } \Pi' = \Pi.
\]

We now show that \( \Pi \vdash e' : t' \) for some \( t' <: t \). \( \Pi \vdash e : t \) implies that \( t \ var = v; e_1 \) and all its subterms are well typed in \( \Pi \). Let \( \Pi \vdash (t \ var = v; e_1) : u \). By (T-Define), \( \Pi, \var : t \vdash e_1 : u' \). By Lemma C.4, \( \Pi \vdash [\var[v]e_1] : u'' \) for some \( u'' <: u' <: u \). Therefore, by lemma C.8, \( \Pi \vdash e' : t' \) for some \( t' <: t \).

The (If-True) and (If-False) rules hold because the stack and the store do not change. Also the type of the entire expression is the same as both the \( if \) expression and the \( else \) expression.

For the (Multi-Register) rule, \( e = E[\text{register}(v)] \) and \( e' = E[v] \). Let \( \tau' = \tau, \mu' = \mu, \psi' = \psi, \gamma' = \gamma \) and \( \Pi' = \Pi \). Obviously, \( t' = t \).

For the (Register) rule, \( e = E[\text{register}(v)] \) and \( e' = E[v] \). Let \( \tau' = \tau, \mu' = \mu, \psi' = \psi, \gamma' = \gamma + v \gamma \) and \( \Pi' = \Pi \). Clearly, \( t' = t \).

For the (Announce) rule, \( e' = E[e_2] \) and \( e = E[\text{announce } p (v_1, \ldots, v_n); e_2] \). Let \( \mu' = \mu, \gamma' = \gamma, \Pi' = \Pi \) and \( t' = t \). Thus \( \Pi \vdash e_2 : t \), has the same type as \( \Pi \vdash \text{yield } e_2 : t \). □

**Definition C.11.** [Thread-interleaving.] If

\[
\langle E[yield e], \tau \rangle + \psi, \mu, \gamma \mapsto \langle E[e_1], \tau_1 \rangle + \psi_1, \mu_1, \gamma_1 \]

\[
\vdash \langle E[e_n], \tau_n \rangle + \psi_n, \mu_n, \gamma_n \mapsto \langle E[e], \tau \rangle + \psi', \mu', \gamma' \text{ or }
\]

\[
\langle E[yield e], \tau \rangle + \psi, \mu, \gamma \mapsto \langle E[e], \tau \rangle + \psi', \mu', \gamma', \gamma' \text{, we denote this as }
\]

\[
\langle E[yield e], \tau \rangle + \psi', \mu', \gamma', \gamma', \text{ where } \forall i \{1 \leq i \leq n \} E_i[e_i] \neq E[e].
\]

**Theorem C.12.** [Subject-reduction-Thread-interleaving.] For an expression \( e = E[yield e_1] \) for any \( e_1 \), \( \tau \) task local, and \( \psi \) a task queue, \( \mu \) a store and \( \gamma \) a handler queue. Let \( \Pi \approx \mu \) be a type environment and \( t \) a type. If \( \Pi \vdash e : t \) and

\[
\langle e, \tau \rangle + \psi, \mu, \gamma \mapsto \langle e', \tau' \rangle + \psi', \mu', \gamma', \gamma' \text{, then } \Pi \approx \mu' \text{ and } \Pi \vdash e' : t' \text{ and } t' <: t.
\]

**Proof Sketch:** The proof is based on the observation that Pānini does not have data race and thus, 1 of Definition C.3 holds. Since the store \( \mu \) does not shrink, 2 of Definition C.3 holds. Clearly, \( \text{yield } e_1 \) and \( e_1 \) have the same type in \( \Pi \), and therefore, by Lemma C.7, if \( \Pi \vdash e : t \), then \( \Pi \vdash e' : t' \). □

**Theorem C.13.** [Soundness.] Given a program

\[
P = decl_1 \ldots decl_n e, \text{ if } \Pi \vdash P : (t, \rho) \text{ for some } t \text{ and } \rho, \text{ then either the evaluation of } e \text{ diverges or else }
\]

\[
\langle e, (0, 0) \rangle \mapsto^* \langle e', \tau' \rangle + \psi', \mu', \gamma' \text{ where one of the following holds for } v:\]

- \( e = \text{null or} \)
- \( e = \text{NullPointerException or} \)
- \( e = \text{ClassCastException.} \)

**Proof Sketch:** If \( e \) diverges, then this case is trivial. Otherwise if \( e \) converges, then because the empty environment is consistent with the empty store. This case is proved by Theorem C.9, Theorem C.10 and Theorem C.12. □